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# MS-7995

ITX:170\*170

Ver: 11

## Intel -SkyLake-S 65W plamform

### CPU:

LGA1151

CPU POWER PAK \*3 Phase

GT POWER PAK \*2 Phase

### System Chipset:

SPT-H :B150 colay H110 colay H170

### Onboard Chip:

HD Audio Codec: ALC887

SIO: NCT5563D

Flash ROM: SPI 128 MB or 64MB

### PWM:

VCORE - RT3606

DDR - RT8231

PCH(1.0V) - RT8125E

VCCSA - RT8125E

### Main Memory:

DDR4 \* 2 (Dual Channel)

### Load Switch:

VCCIO - SLG59M1457V 6A

### ACPI:

5VDAUL:uP7501

5VDIMM:uP7501

3VSB:GS7133+PN MOS

3VDSW:GS7133

VCCSTPLL - GS7133

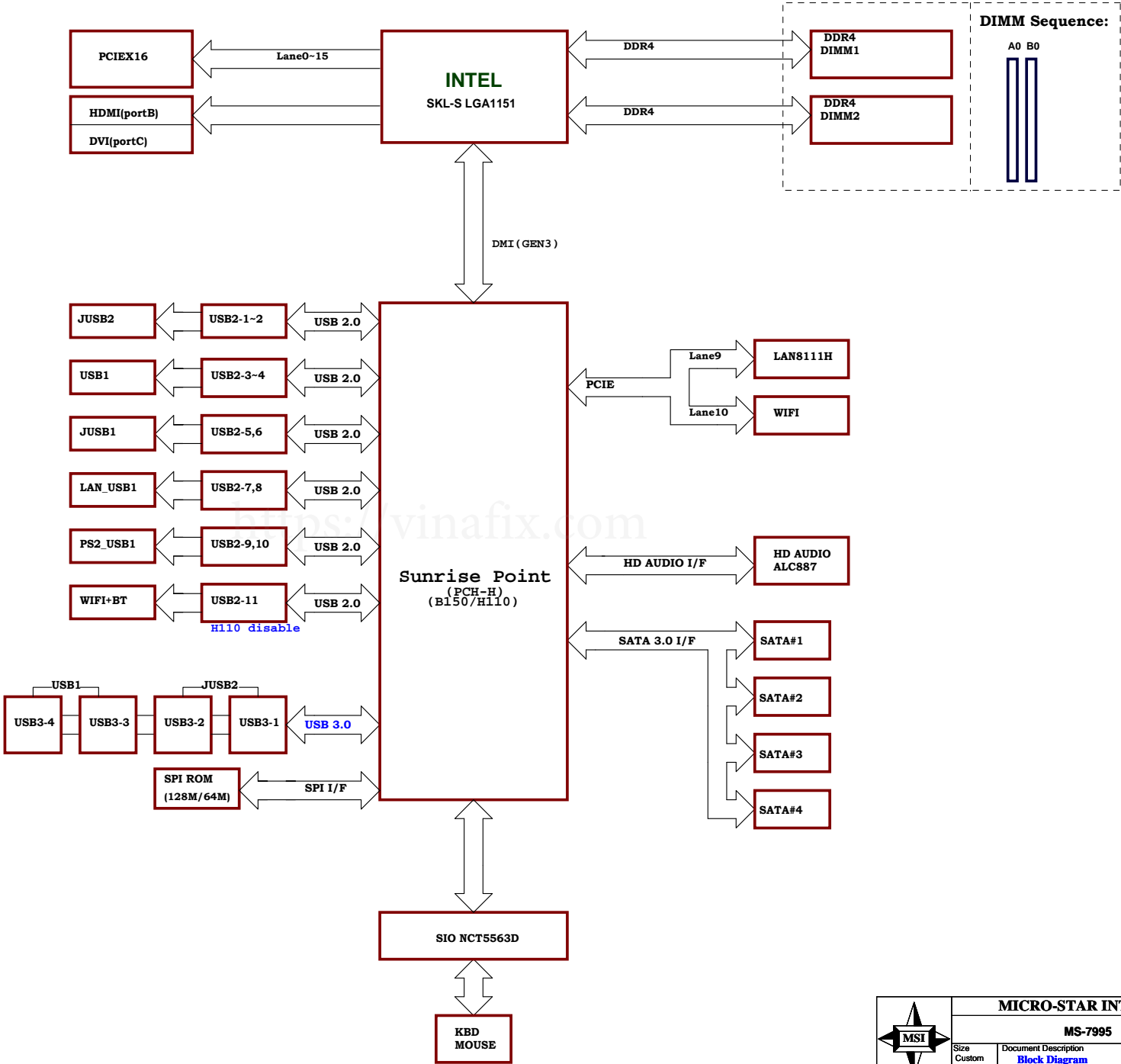
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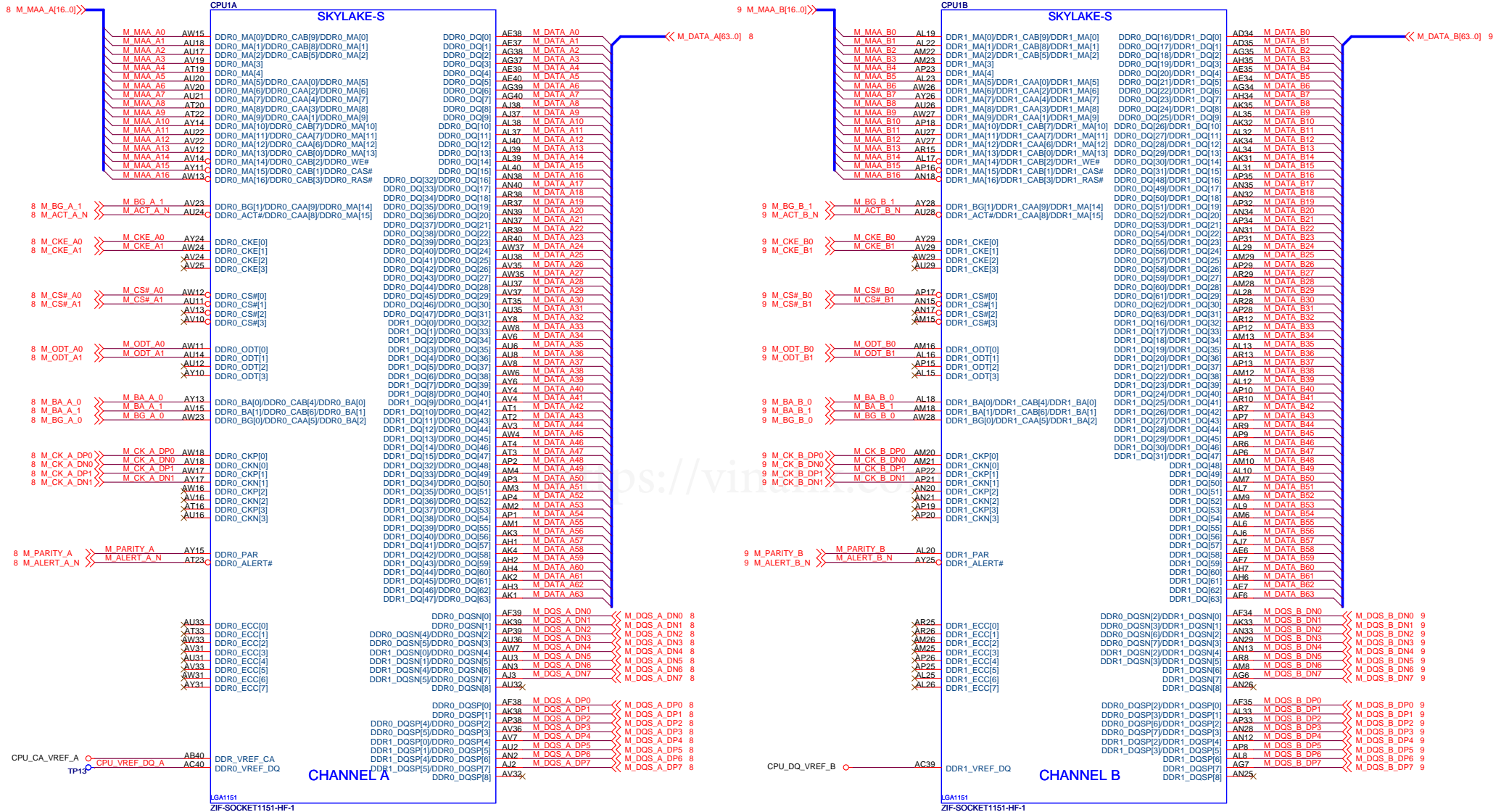
PCI Express (X16) Slot \* 1



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MS-7995 Block Diagram





VCCIO R2 24.9R1%4 PEG\_COMP L7  
L<=0.4 inch

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CPU1C

SKYLAKE-S

19 EXP_A_RXP_0	B6	PEG_RXP[0]	PEG_TXP[0]	A5	EXP_A_TXP_0	19
19 EXP_A_RXN_0	B7	PEG_RXN[0]	PEG_TXN[0]	A6	EXP_A_TXN_0	19
19 EXP_A_RXP_1	C7	PEG_RXP[1]	PEG_TXP[1]	B4	EXP_A_TXP_1	19
19 EXP_A_RXN_1	C6	PEG_RXN[1]	PEG_TXN[1]	B5	EXP_A_TXN_1	19
19 EXP_A_RXP_2	D6	PEG_RXP[2]	PEG_TXP[2]	C3	EXP_A_TXP_2	19
19 EXP_A_RXN_2	D5	PEG_RXN[2]	PEG_TXN[2]	C4	EXP_A_TXN_2	19
19 EXP_A_RXP_3	E5	PEG_RXP[3]	PEG_TXP[3]	D2	EXP_A_TXP_3	19
19 EXP_A_RXN_3	E4	PEG_RXN[3]	PEG_TXN[3]	D3	EXP_A_TXN_3	19
19 EXP_A_RXP_4	F6	PEG_RXP[4]	PEG_TXP[4]	E1	EXP_A_TXP_4	19
19 EXP_A_RXN_4	F5	PEG_RXN[4]	PEG_TXN[4]	E2	EXP_A_TXN_4	19
19 EXP_A_RXP_5	G6	PEG_RXP[5]	PEG_TXP[5]	F3	EXP_A_TXP_5	19
19 EXP_A_RXN_5	G4	PEG_RXN[5]	PEG_TXN[5]	G1	EXP_A_TXP_6	19
19 EXP_A_RXP_6	H6	PEG_RXP[6]	PEG_TXP[6]	G2	EXP_A_TXN_6	19
19 EXP_A_RXN_6	H5	PEG_RXN[6]	PEG_TXN[6]	H2	EXP_A_TXP_7	19
19 EXP_A_RXP_7	J6	PEG_RXP[7]	PEG_TXP[7]	H3	EXP_A_TXN_7	19
19 EXP_A_RXN_7	J4	PEG_RXN[7]	PEG_TXN[7]	J1	EXP_A_TXP_8	19
19 EXP_A_RXP_8	K6	PEG_RXP[8]	PEG_TXP[8]	J2	EXP_A_TXN_8	19
19 EXP_A_RXN_8	K5	PEG_RXN[8]	PEG_TXN[8]	K3	EXP_A_TXP_9	19
19 EXP_A_RXP_9	L6	PEG_RXP[9]	PEG_TXP[9]	L1	EXP_A_TXN_9	19
19 EXP_A_RXN_9	M6	PEG_RXN[9]	PEG_TXN[9]	L2	EXP_A_TXP_10	19
19 EXP_A_RXP_10	M5	PEG_RXP[10]	PEG_TXP[10]	M2	EXP_A_TXN_10	19
19 EXP_A_RXN_10	N6	PEG_RXN[10]	PEG_TXN[10]	M3	EXP_A_TXP_11	19
19 EXP_A_RXP_11	N5	PEG_RXP[11]	PEG_TXP[11]	N1	EXP_A_TXN_11	19
19 EXP_A_RXN_11	N4	PEG_RXN[11]	PEG_TXN[11]	N2	EXP_A_TXP_12	19
19 EXP_A_RXP_12	P6	PEG_RXP[12]	PEG_TXP[12]	P2	EXP_A_TXN_12	19
19 EXP_A_RXN_12	P5	PEG_RXN[12]	PEG_TXN[12]	R2	EXP_A_TXP_13	19
19 EXP_A_RXP_13	R6	PEG_RXP[13]	PEG_TXP[13]	R3	EXP_A_TXN_13	19
19 EXP_A_RXN_13	R4	PEG_RXN[13]	PEG_TXN[13]	R1	EXP_A_TXP_14	19
19 EXP_A_RXP_14	T6	PEG_RXP[14]	PEG_TXP[14]	T2	EXP_A_TXN_14	19
19 EXP_A_RXN_14	T5	PEG_RXN[14]	PEG_TXN[14]	T3	EXP_A_TXP_15	19
19 EXP_A_RXP_15	U6	PEG_RXP[15]	PEG_TXP[15]	T1	EXP_A_TXN_15	19
19 EXP_A_RXN_15	U4	PEG_RXN[15]	PEG_TXN[15]			

12 DMI_RXP0	Y3	DMI_RXP[0]	DMI_TXP0	AC2	DMI_TXP0	12
12 DMI_RXN0	Y4	DMI_RXN[0]	DMI_TXN0	AC1	DMI_TXN0	12
12 DMI_RXP1	AA4	DMI_RXP[1]	DMI_TXP1	AD3	DMI_TXP1	12
12 DMI_RXN1	AA5	DMI_RXN[1]	DMI_TXN1	AD2	DMI_TXN1	12
12 DMI_RXP2	AB4	DMI_RXP[2]	DMI_TXP2	AE2	DMI_TXP2	12
12 DMI_RXN2	AB3	DMI_RXN[2]	DMI_TXN2	AE1	DMI_TXN2	12
12 DMI_RXP3	AC4	DMI_RXP[3]	DMI_TXP3	AF2	DMI_TXP3	12
12 DMI_RXN3	AC5	DMI_RXN[3]	DMI_TXN3	AF3	DMI_TXN3	12

GA1151

ZIF-SOCKET1151-HF-1

CPU1D

SKYLAKE-S

EDP_TXP[0]	DDI1_TXP[0]	C21	HDMI_DDPB_TX2_P	HDMI_DDPB_TX2_P	26
EDP_TXN[0]	DDI1_TXN[0]	D21	HDMI_DDPB_TX2_N	HDMI_DDPB_TX2_N	26
EDP_TXP[1]	DDI1_TXP[1]	D22	HDMI_DDPB_TX1_P	HDMI_DDPB_TX1_P	26
EDP_TXN[1]	DDI1_TXN[1]	E22	HDMI_DDPB_TX1_N	HDMI_DDPB_TX1_N	26
EDP_TXP[2]	DDI1_TXP[2]	B23	HDMI_DDPB_TX0_P	HDMI_DDPB_TX0_P	26
EDP_TXN[2]	DDI1_TXN[2]	A23	HDMI_DDPB_TX0_N	HDMI_DDPB_TX0_N	26
EDP_TXP[3]	DDI1_TXP[3]	C23	HDMI_DDPB_CLK_P	HDMI_DDPB_CLK_P	26
EDP_TXN[3]	DDI1_TXN[3]	D23	HDMI_DDPB_CLK_N	HDMI_DDPB_CLK_N	26
EDP_AUXP	DDI1_AUXP	B13			
EDP_AUXN	DDI1_AUXN	C13			
EDP_DISP_UTIL	DDI2_TXP[0]	B18	DVI_DDPC_TXP2	DVI_DDPC_TXP2	25
EDP_RCOMP	DDI2_TXN[0]	A18	DVI_DDPC_TXN2	DVI_DDPC_TXN2	25
	DDI2_TXP[1]	E18	DVI_DDPC_TXP1	DVI_DDPC_TXP1	25
	DDI2_TXN[1]	C19	DVI_DDPC_TXP0	DVI_DDPC_TXP0	25
	DDI2_TXP[2]	D19	DVI_DDPC_TXN0	DVI_DDPC_TXN0	25
	DDI2_TXN[2]	D20	DVI_DDPC_CLK_P	DVI_DDPC_CLK_P	25
	DDI2_TXP[3]	E20	DVI_DDPC_CLK_N	DVI_DDPC_CLK_N	25
	DDI2_TXN[3]				
	DDI2_AUXP	A12			
	DDI2_AUXN	B12			
	DDI3_TXP[0]	B14			
	DDI3_TXN[0]	A14			
	DDI3_TXP[1]	C15			
	DDI3_TXN[1]	B15			
	DDI3_TXP[2]	A16			
	DDI3_TXN[2]	A16			
	DDI3_TXP[3]	C17			
	DDI3_TXN[3]	B17			
	DDI3_AUXP	B11			
	DDI3_AUXN	C11			

DVI max resolution is 1920x1200 @ 60 Hz

MEC1 MEC1  
MEC2 MEC2  
MEC3 MEC3  
MEC4 MEC4  
MEC5 MEC5  
MEC6 MEC6  
MEC7 MEC7

GA1151

ZIF-SOCKET1151-HF-1

CPU1F

SKYLAKE-S

VSS-372	RSVD-20	AC37	
RSVD-2			
RSVD-3			
RSVD-4			
RSVD-5			
RSVD-6			
VSS-373			
RSVD-23			
RSVD-9			
RSVD-10			
RSVD-11			
RSVD-12			
RSVD-13			
RSVD-14			
RSVD-15			
RSVD-16			
RSVD-17			
RSVD-18			
RSVD-19			
RSVD_TP-1			
RSVD_TP-2			
RSVD_TP-3			
RSVD_TP-4			
RSVD_TP-5			
RSVD_TP-6			
RSVD_TP-7			
RSVD_TP-8			
RSVD_TP-9			
RSVD_TP-10			

GA1151

ZIF-SOCKET1151-HF-1

2014.09.24

CRB 1.0 update

CRB unstuff

PCB come back remove



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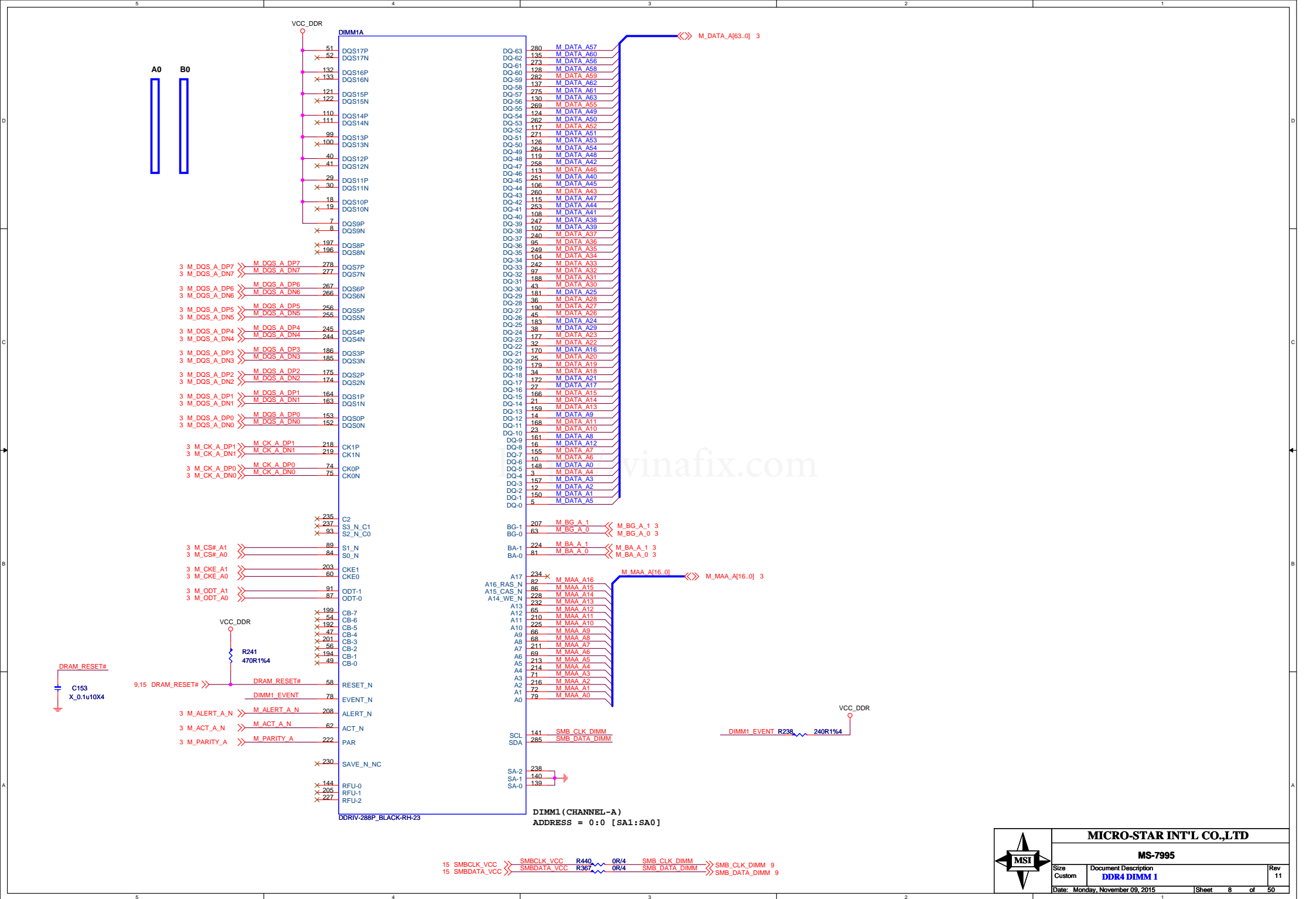
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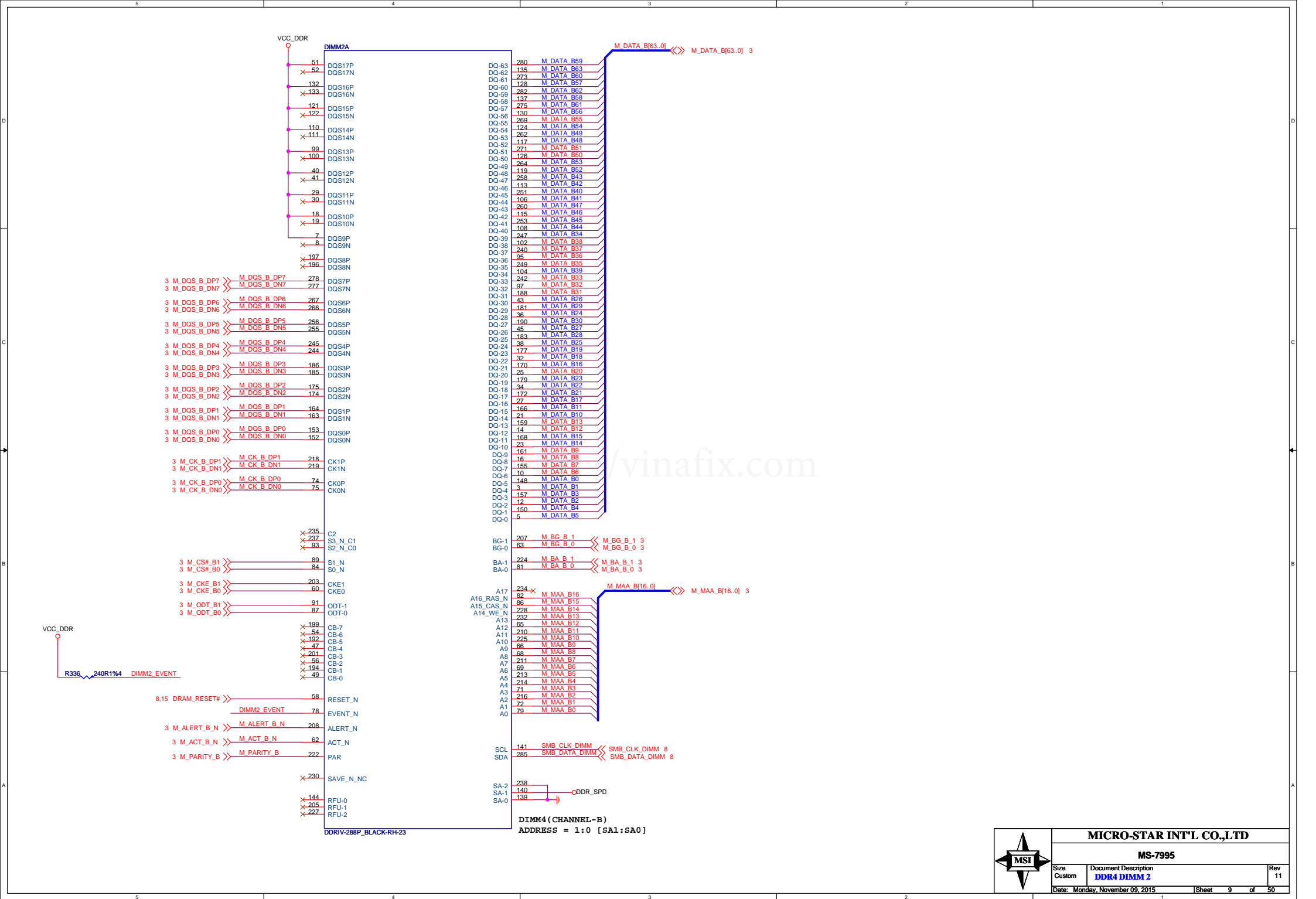


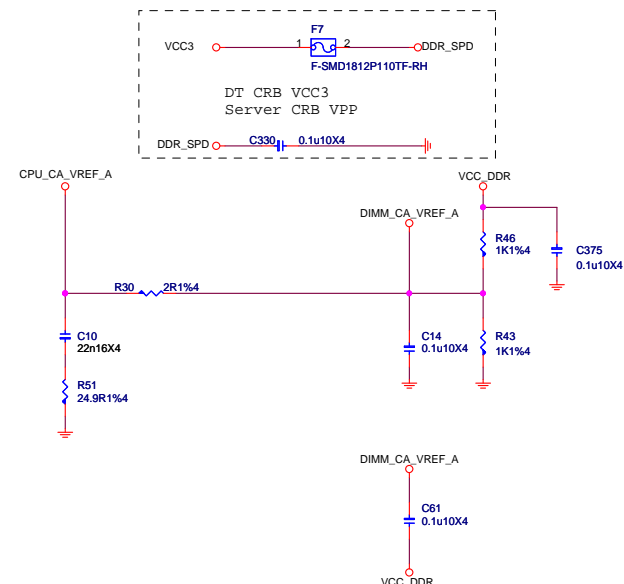
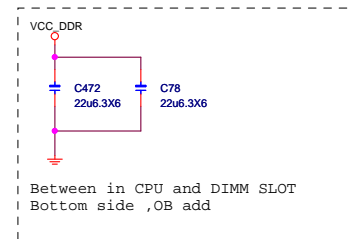
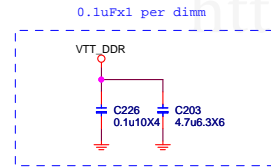
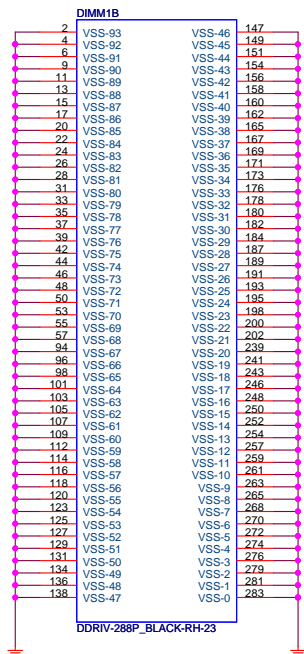
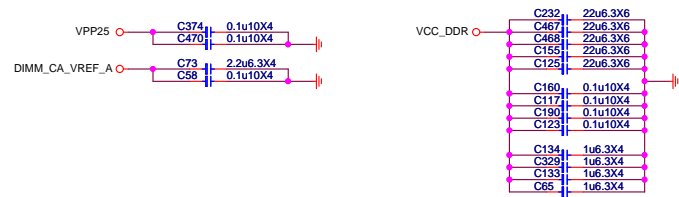
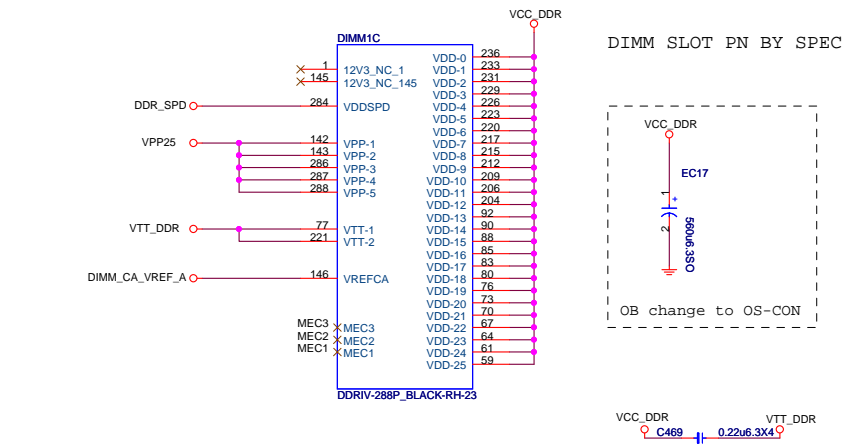




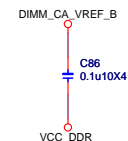
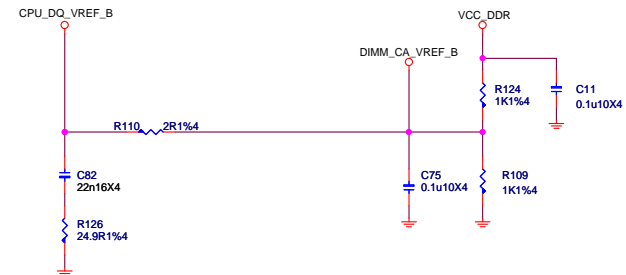
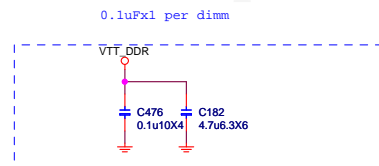
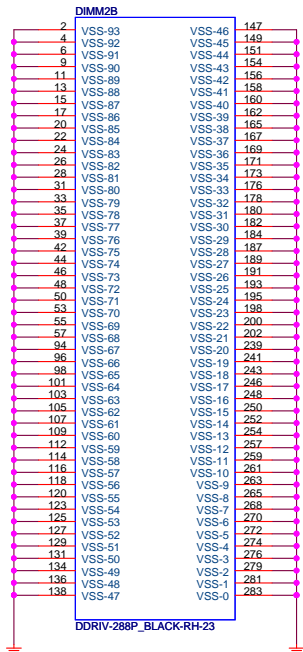
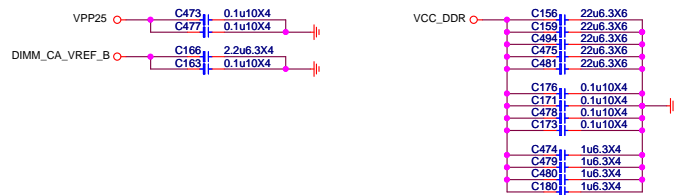
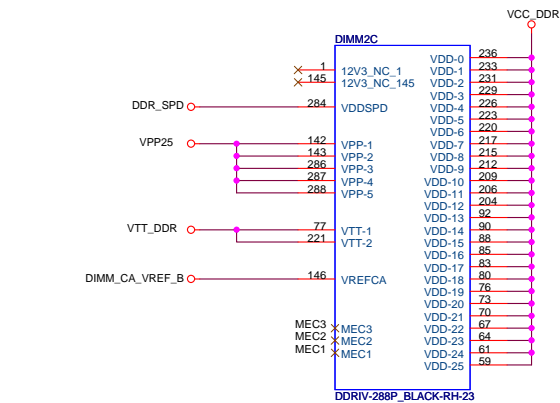


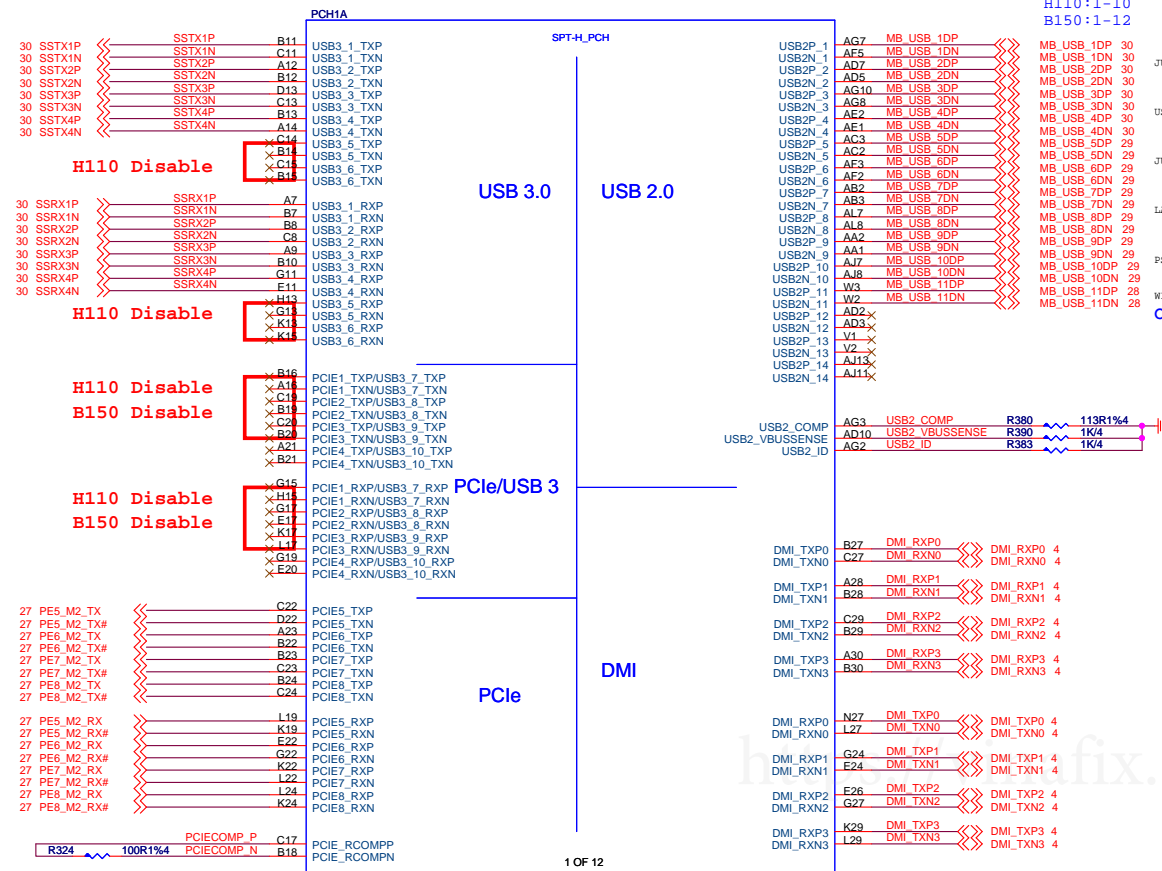




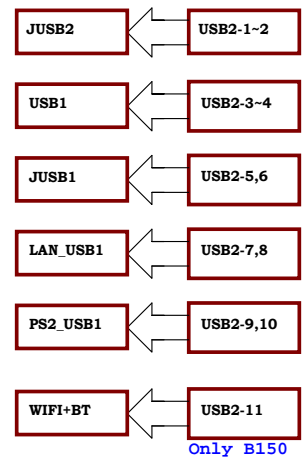


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H110:1-10  
B150:1-12

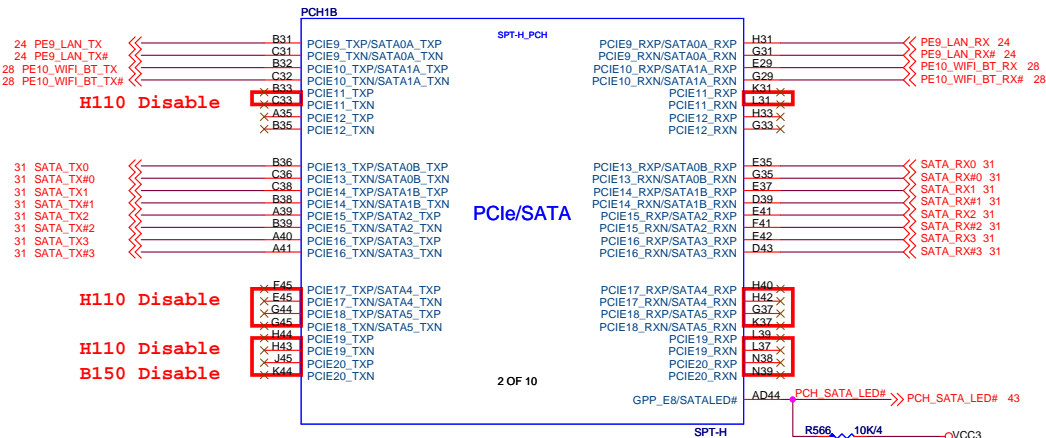


USB2\_COMP < 500 mil

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B150	USB3/ OTG	USB3/ SSIC	USB3/ SSIC	USB3	USB3	USB3	N/A	N/A	N/A	LAN Only	PCle/ LAN	PCle	PCle	PCle
H170	USB3/ OTG	USB3/ SSIC	USB3/ SSIC	USB3	USB3	USB3	USB3	USB3	PCle	PCle/ LAN	PCle/ LAN	PCle	PCle	PCle
Z170	USB3/ OTG	USB3/ SSIC	USB3/ SSIC	USB3	USB3	USB3	USB3/ PCle	USB3/ PCle	USB3/ PCle	PCle/ LAN	PCle/ LAN	PCle	PCle	PCle

Sku	15	16	17	18	19	20	21	22	23	24	25	26	RST for PCIe Ports
H110	PCle/ LAN	PCle	N/A	LAN	SATA*/ LAN	SATA*	SATA	SATA	N/A	N/A	N/A	N/A	0
B150	PCle/LAN	PCle/ SATA*	PCle	PCle/ LAN	SATA*	SATA	SATA	SATA	SATA	N/A	N/A	N/A	0
H170	SATA	SATA	PCle	LAN	SATA	SATA	SATA	SATA	SATA	PCle	PCle	PCle	2
Z170	SATA	SATA	PCle	LAN	SATA	SATA	SATA	SATA	SATA	PCle	PCle	PCle	3

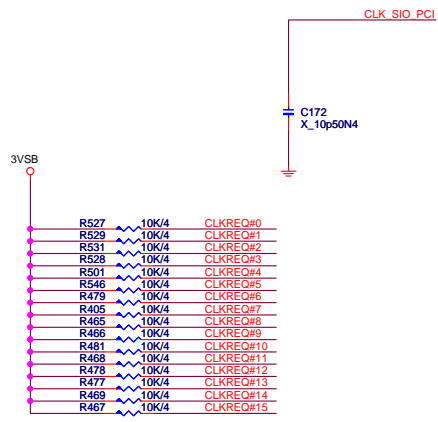
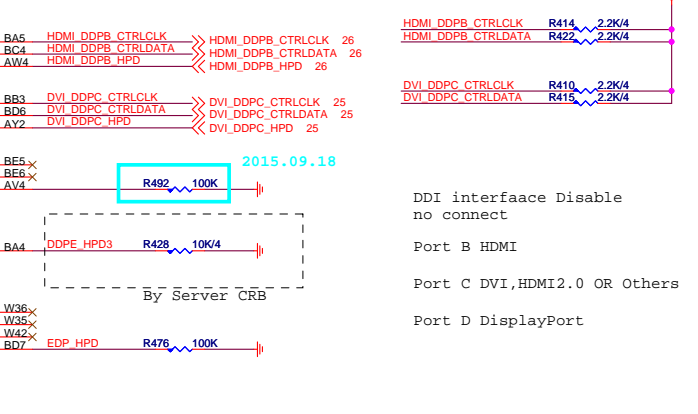
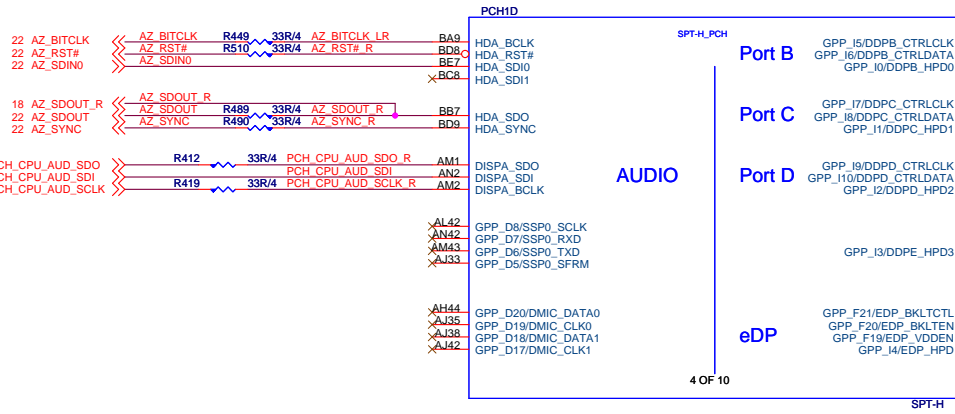
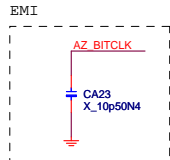
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PCIECOMP\_N



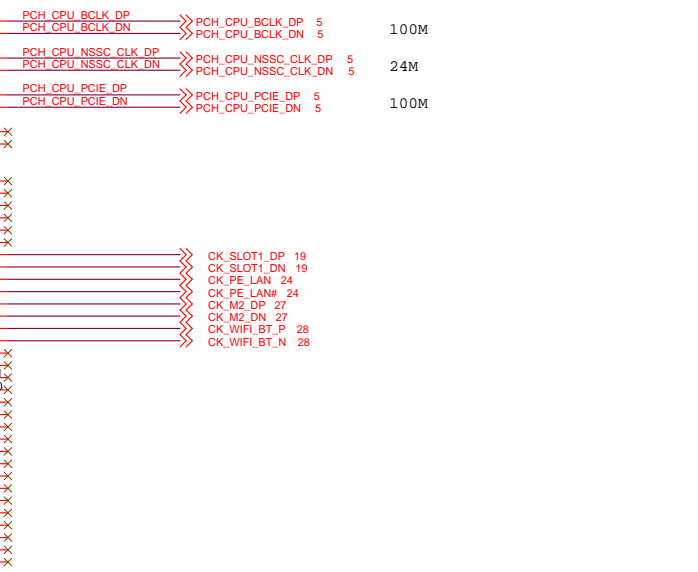
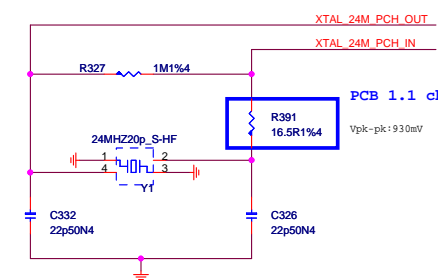
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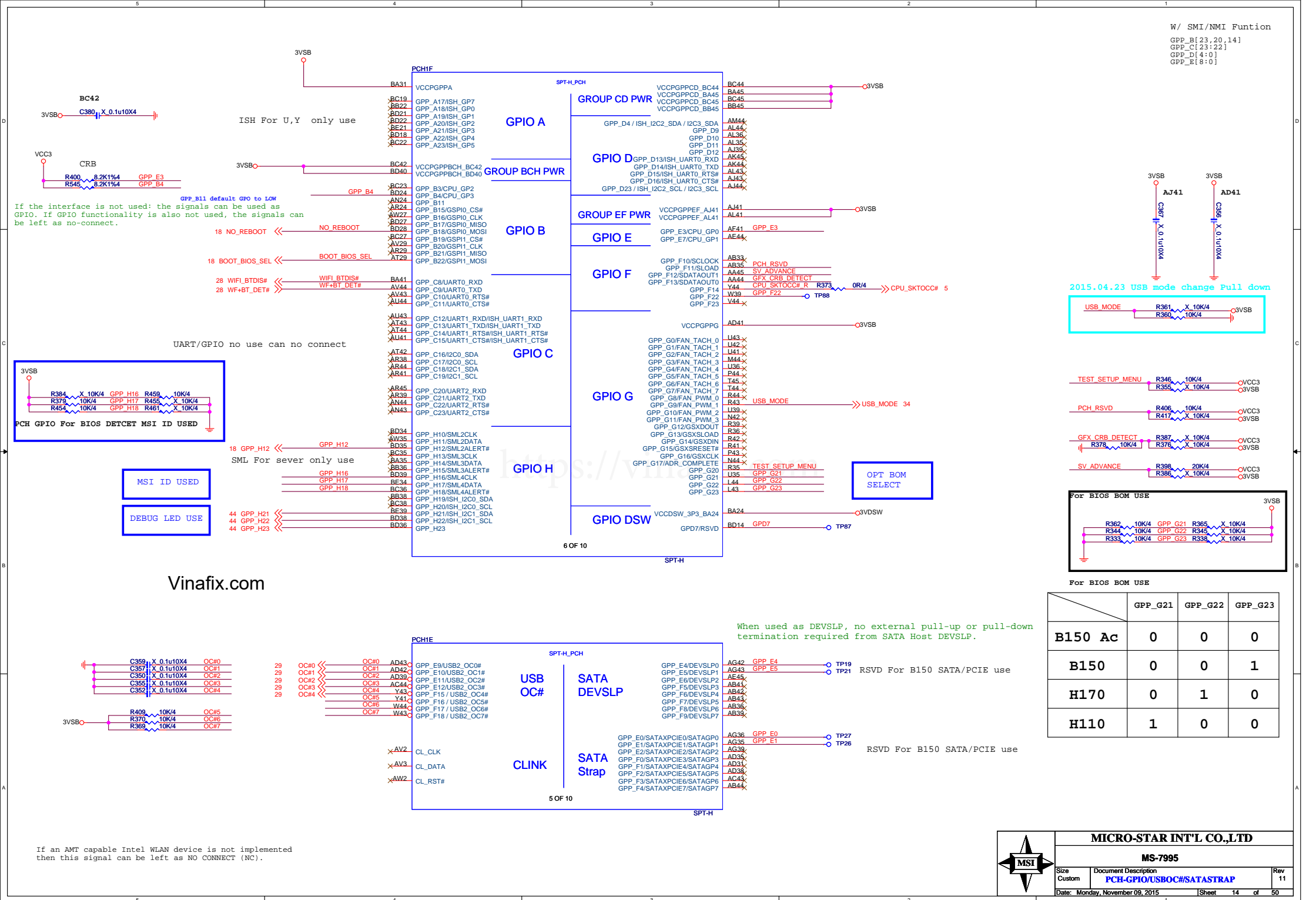
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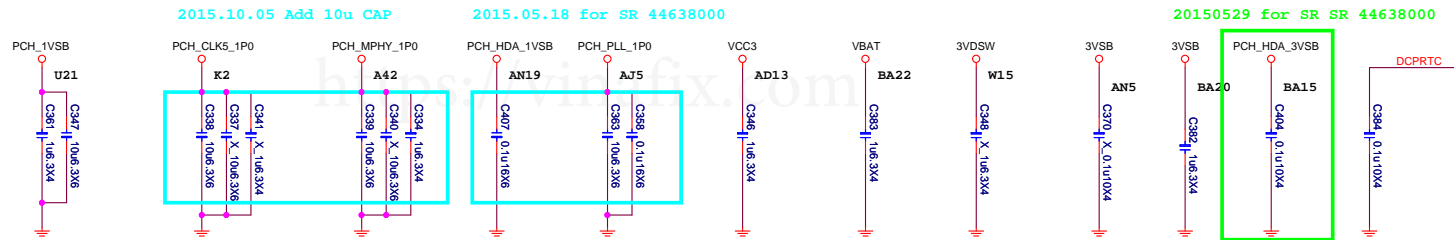
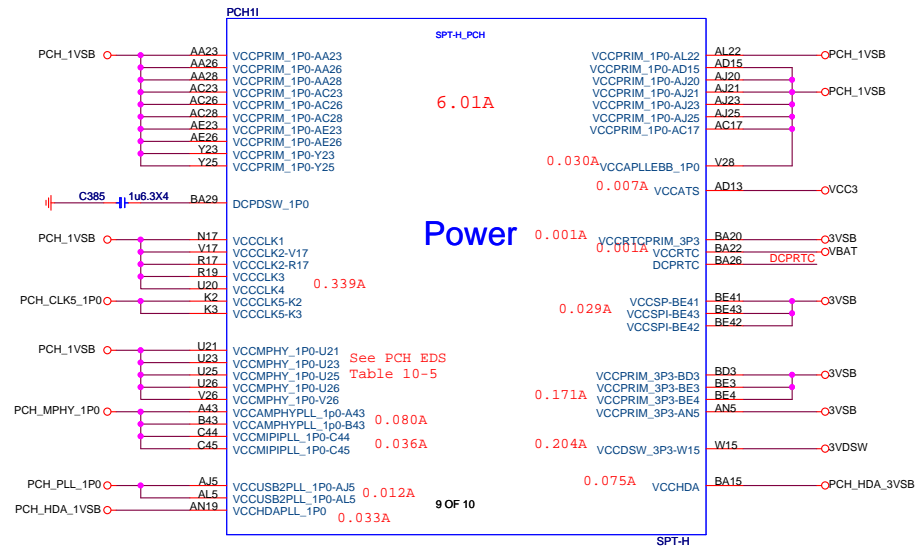
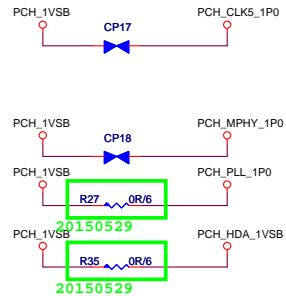
Comtact to SLOT Pin B12  
for support L1 PM Substates  
ME also can disable this funtion.

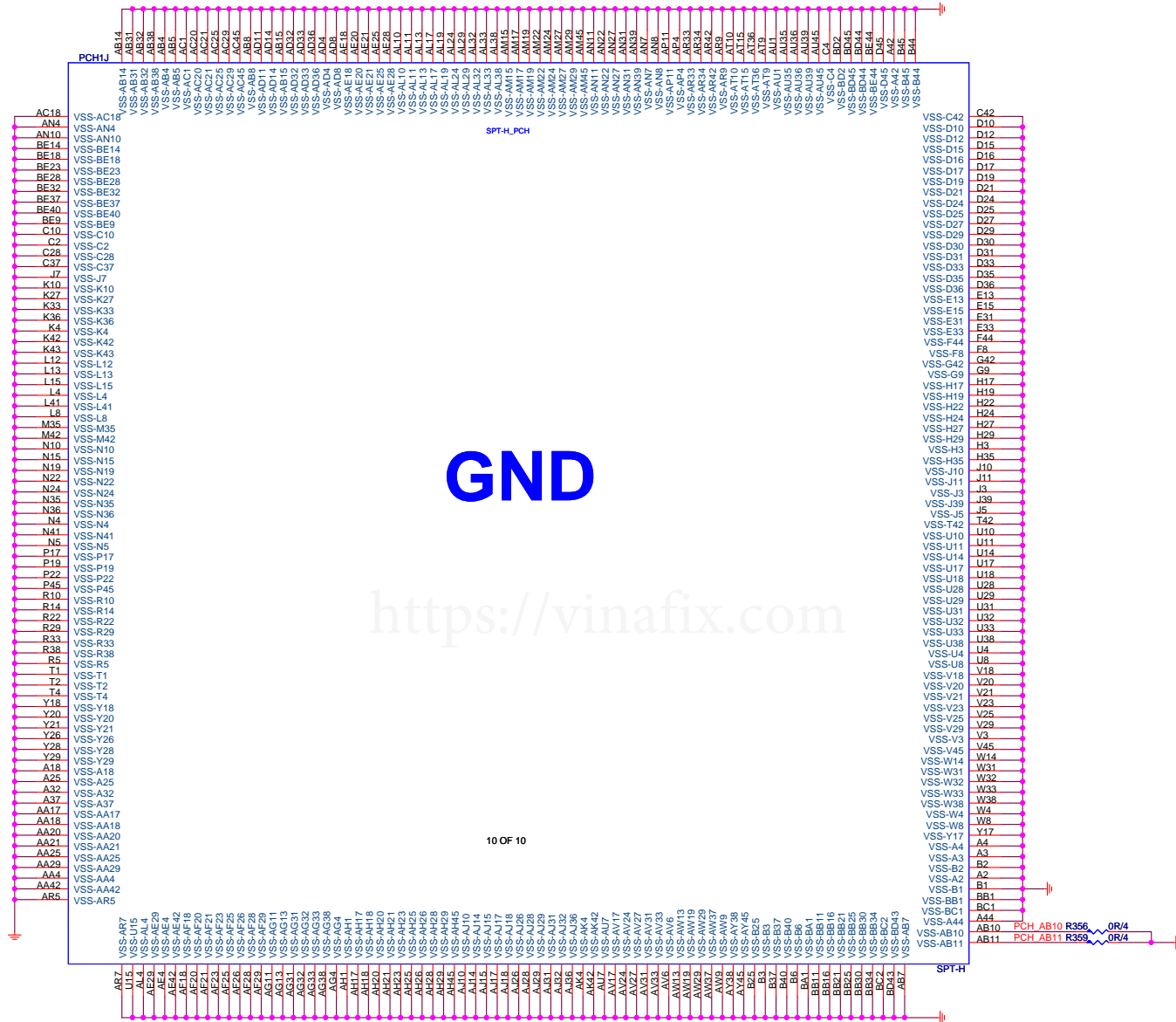




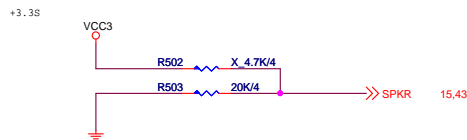






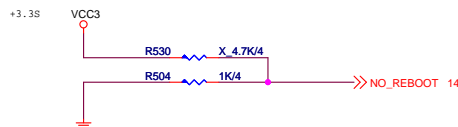


# TOP Swap



Internal pull-down 20K is disabled after PLTRST#

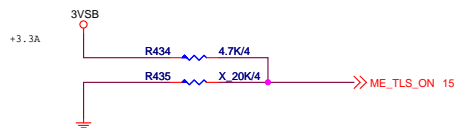
# No Reboot



0 : DISABLE (Default)  
1 : ENABLE

Internal pull-down 20K is disabled after PLTRST#

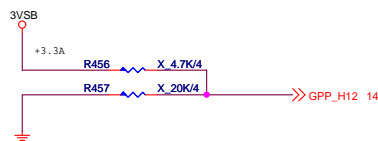
# AMT and SBA with confidentiality



0 : DISABLE  
1 : ENABLE (Default)

Internal pull-down 20K is disabled after RSMRST

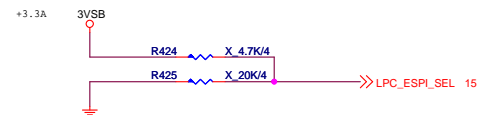
# ESPI FLASH SHARING MODE



0 : MASTER ATTACHED FLASH SHARING  
1 : SLAVE ATTACHED FLASH SHARING

Internal pull-down 20K is disabled after RSMRST

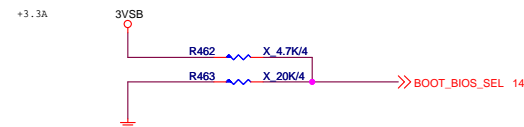
# LPC eSPI Mode



0 : LPC  
1 : eSPI

Internal pull-down 20K is disabled after RSMRST

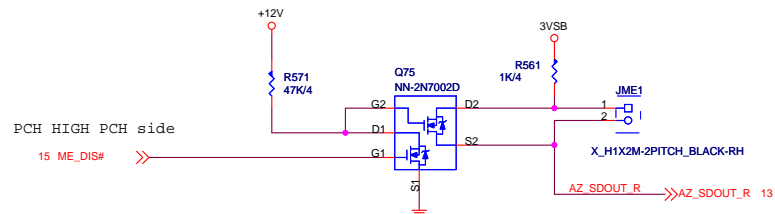
# Boot BIOS



0 : SPI  
1 : LPC

Internal pull-down 20K is disabled after PLTRST

# HDA\_SDO



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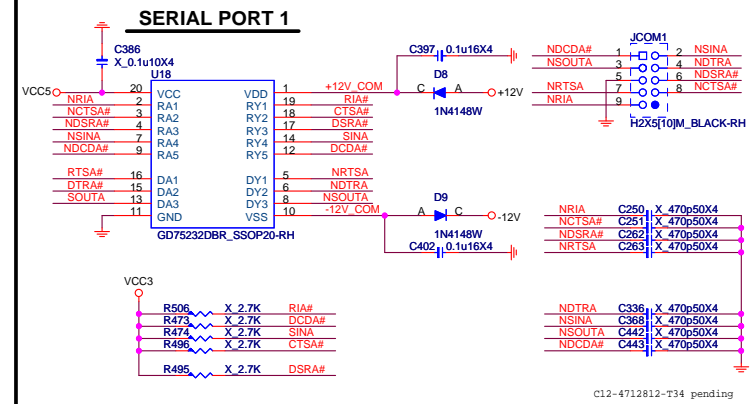
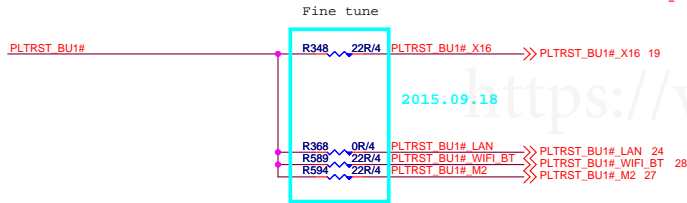
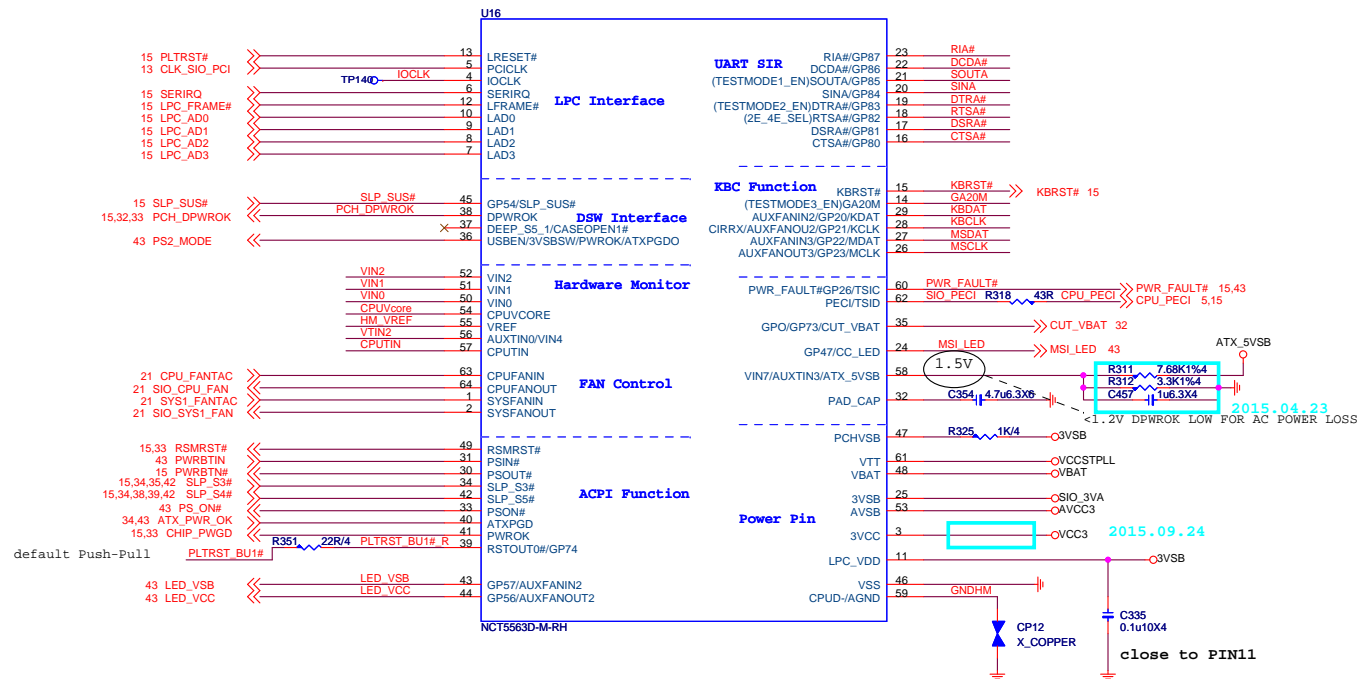
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Custom	PCH-Strap	11
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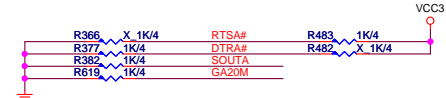
MS-7995

Size Custom	Document Description <b>PCIE SLOT-CPU(X16)</b>	Rev 11
Date: Monday, November 09, 2015		Sheet 19 of 50

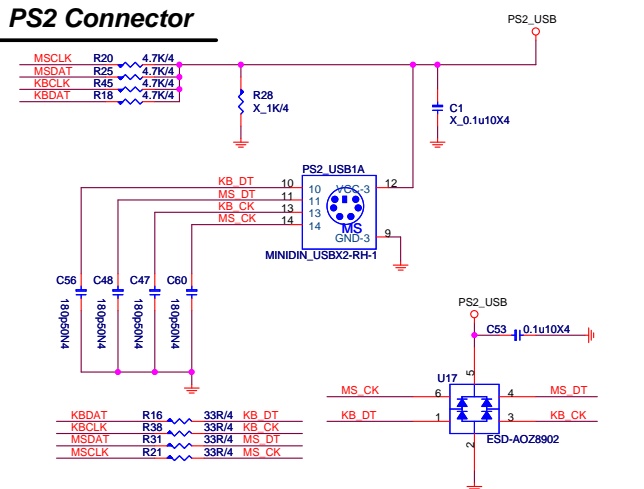


## POWER ON STRAPPING PIN FOR NCT5563D

PIN	5563D NAME	Circuit NAME	0	1
18	2E_4E_SEL	RTSA#	I/O ADDRESS 2E	I/O ADDRESS 4E
19	24M_48M_SEL	DTRA#	24M CLOCK SOURCE	48M CLOCK SOURCE
21	TESTMODE1_EN	SOUTA	DISABLE TESTMODE	ENABLE TESTMODE



## PS2 Connector

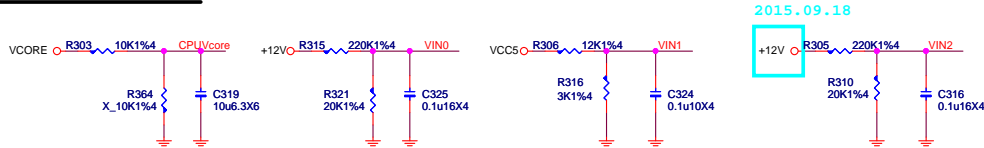


**MICRO-STAR INT'L CO.,LTD**

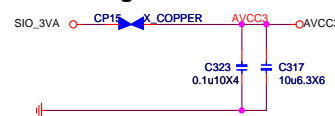
**MS-7995**

Size	Document Description	Rev
Custom	<b>SIO-NCT5563D-M</b>	11
Date: Monday, November 09, 2015	Sheet 20 of 50	

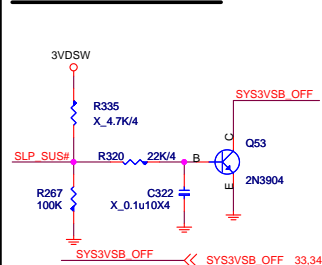
## HW Monitor - Voltage



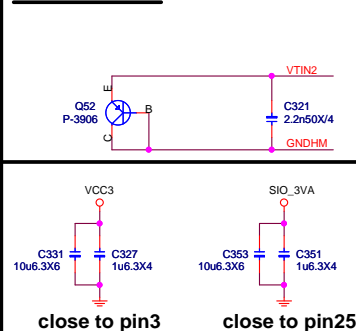
## 3V Analog Power



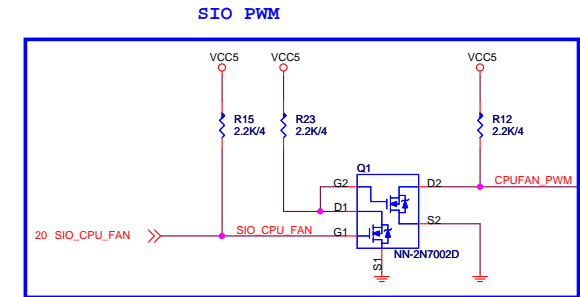
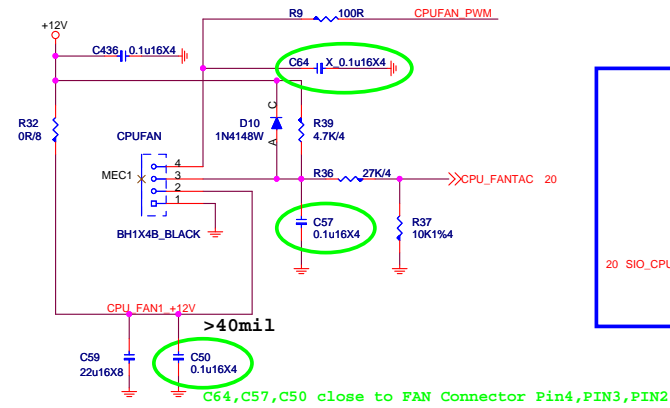
## 5563D DSW SUPPORT



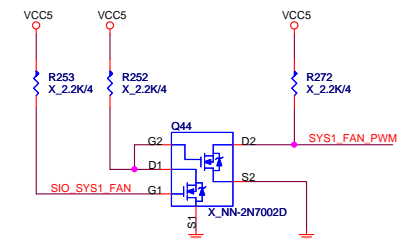
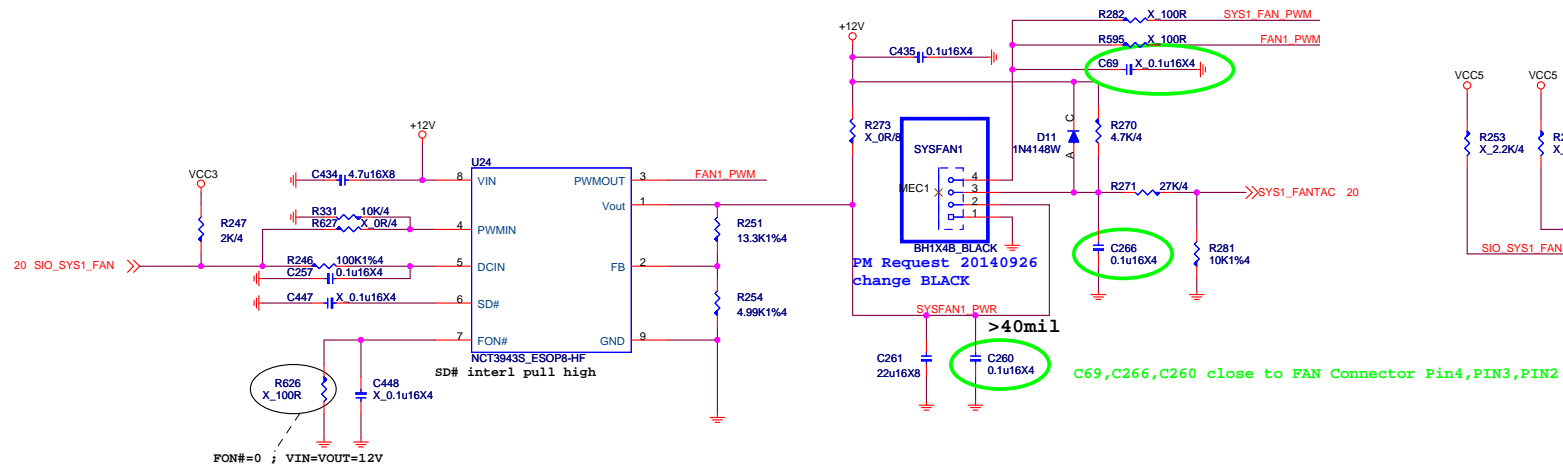
## Thermal Monitor



## Type G : 4 PIN CPU FAN FROM SIO



## Type H : 4 PIN SYS FAN FROM SIO

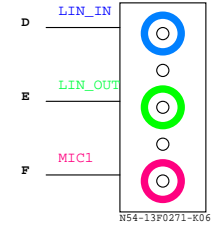
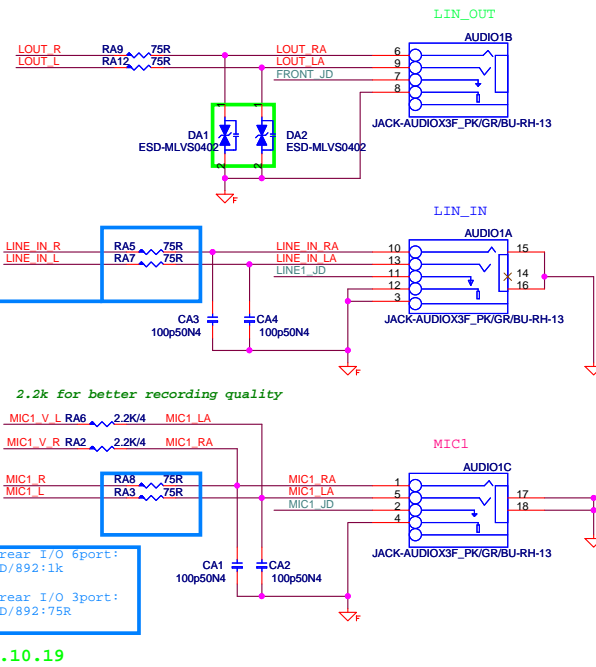
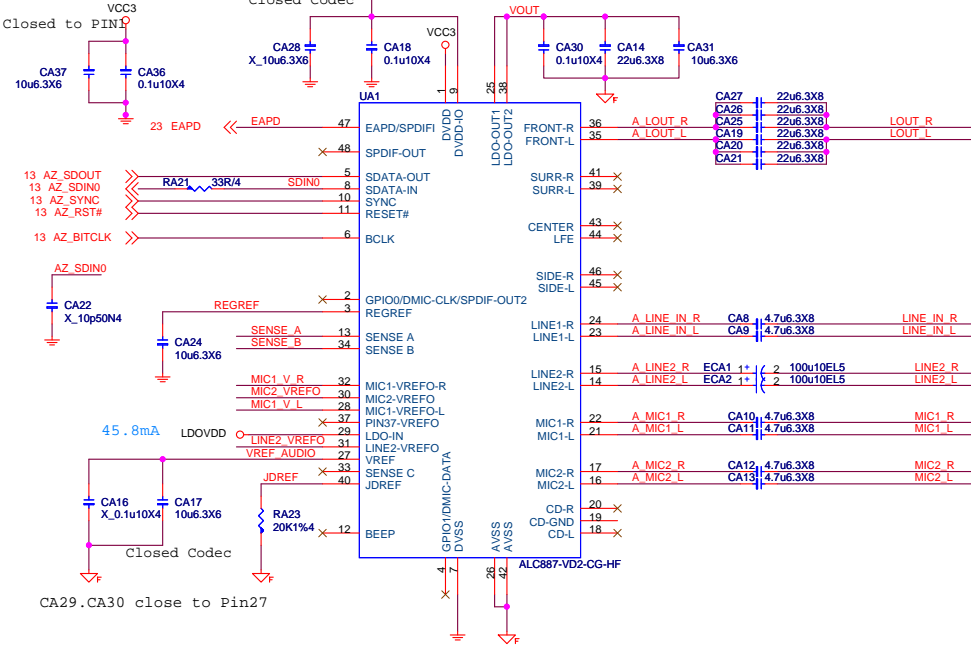


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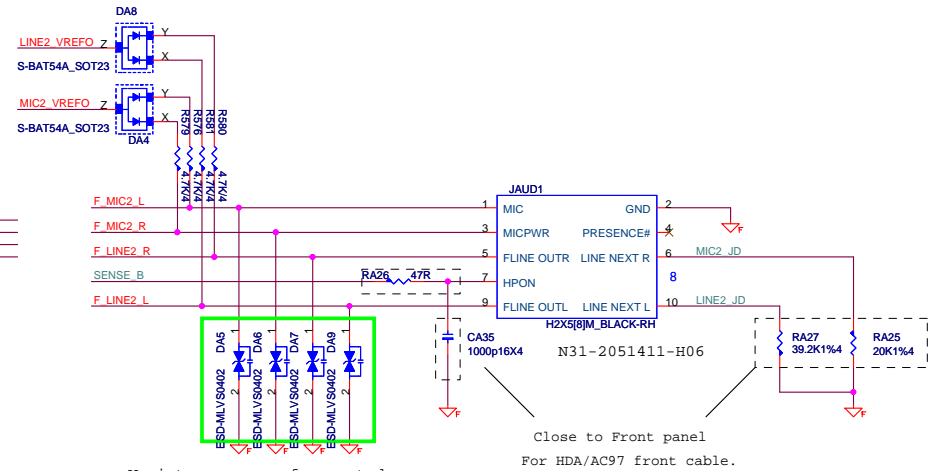
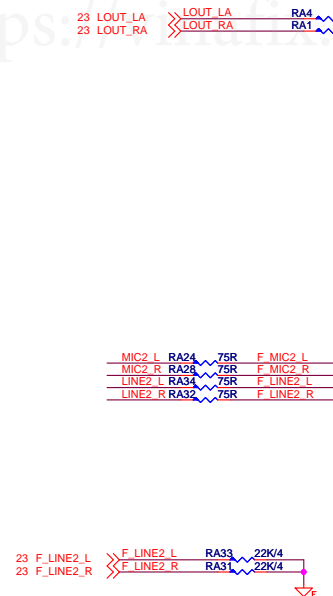
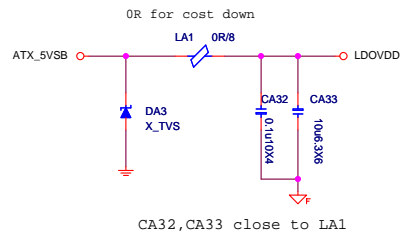
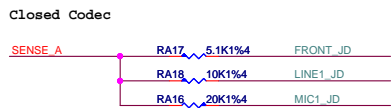
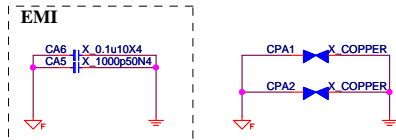
**MS-7995**

Size Custom	Document Description <b>FAN CONTROLLOR 1G+1H</b>	Rev 11
Date: Monday, November 09, 2015		Sheet 21 of 50

# Type B: ALC892/887



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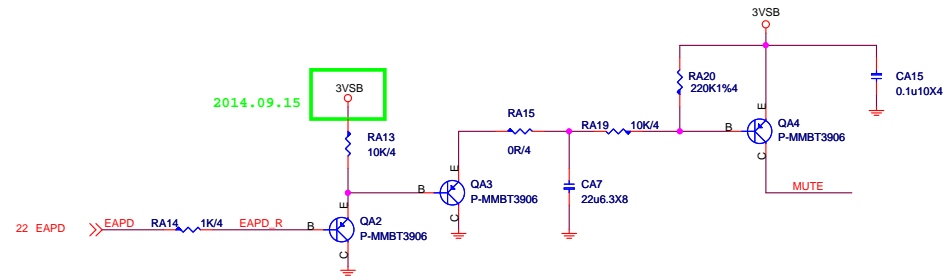
Varistor --> cap for cost down  
D0G-2950500-SI0  
D0G-3010510-I05  
Close to Jack

MSI			
MICRO-STAR INT'L CO.,LTD			
MS-7995			
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Custom	AUDIO - ALC887	11	
Date: Monday, November 09, 2015	Sheet 22 of 50		



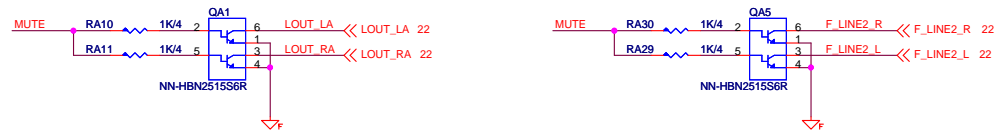
# Rear Line OUT De-POP circuit

De-pop circuit for Rear Line out & Front Headphone out)



Digital

Analog



<https://vinafix.com>

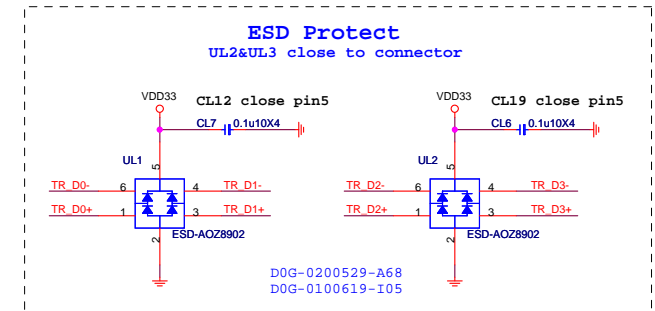
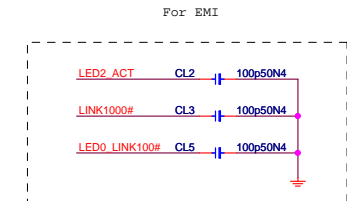
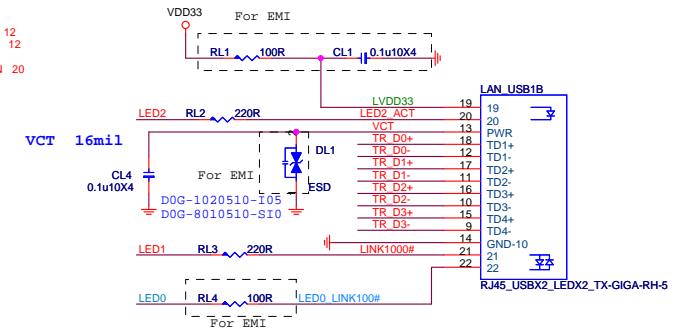
History:

2014/02/13: stuff de-pop circuit of Line out & HP out.

# RTL8111G/RTL8111H Giga LAN

8111H:B06-08111CC-R09  
8111G:B06-081116C-R09

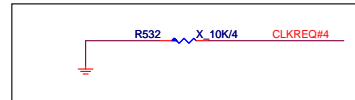
## LAN Connector



Pin33: 4 via from top layer to GND layer and make the via at the center of IC.

<https://vinafix.com>

For module unstuff



### 8111G POWER Consumption

	3.3V @ mA	mW
10 M Idle/TxRx	17.15/116.7	56.6/385.1
100 M Idle/TxRx	71.45/129.5	235.8/427.4
Giga Idle/TxRx	179.1/243.9	591/804.9
ALDPS	6.41	21.15

### 8111H POWER Consumption

	3.3V @ mA	mW
10 M Idle/TxRx	9.9/84.69	32.67/279.48
100 M Idle/TxRx	48.11/92.44	158.76/305.05
Giga Idle/TxRx	124.5/177.57	410.85/585.98
ALDPS	5.50	18.15

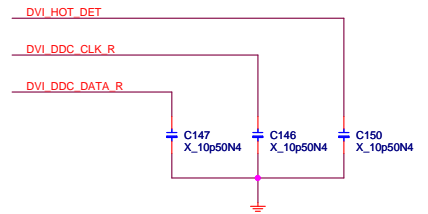
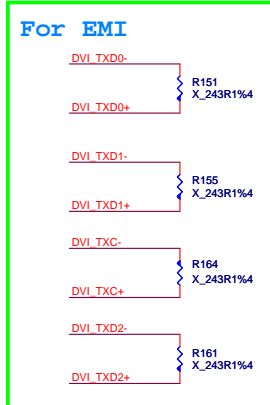
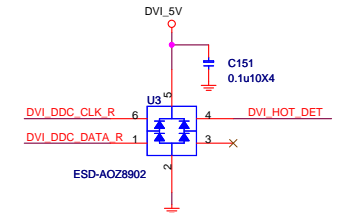
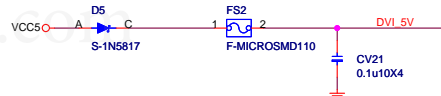
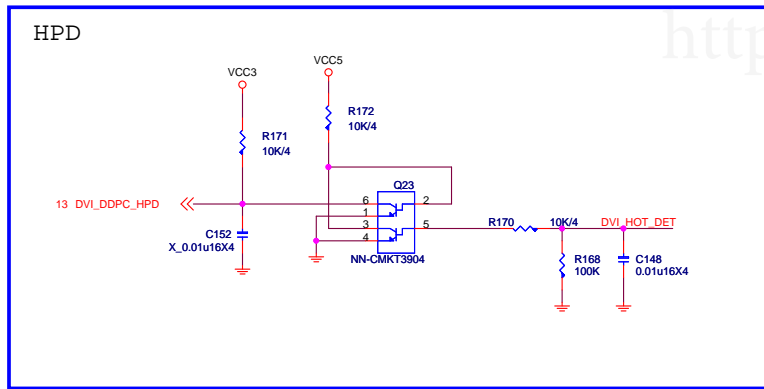
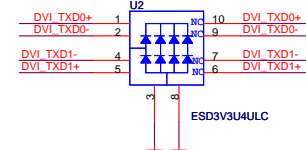
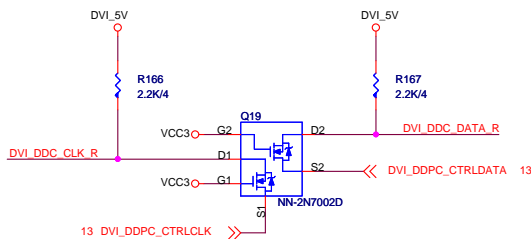
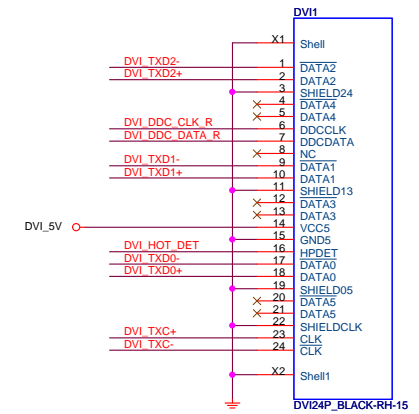
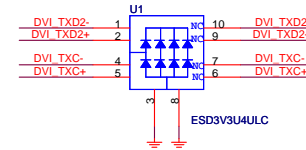
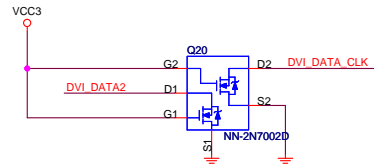
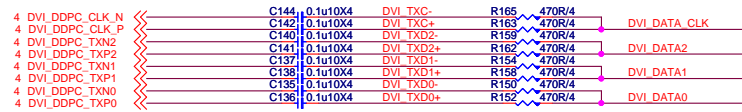


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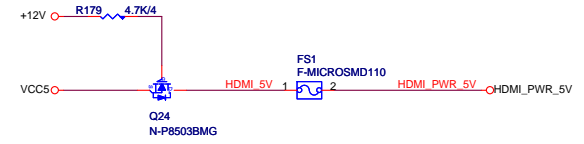
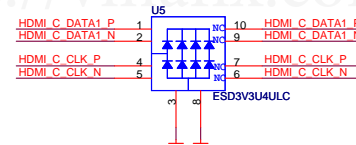
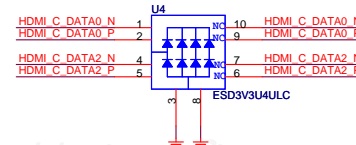
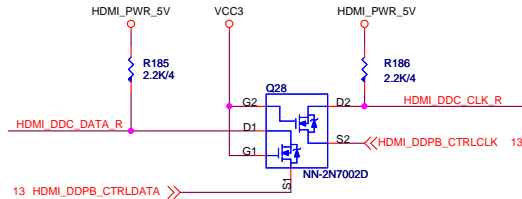
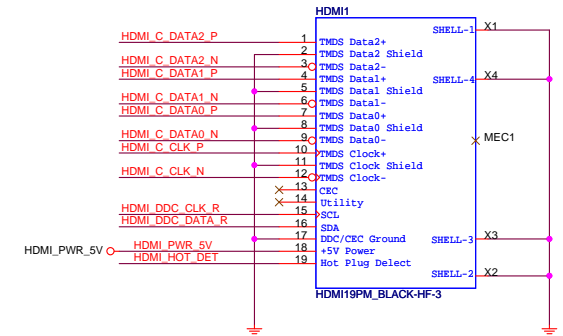
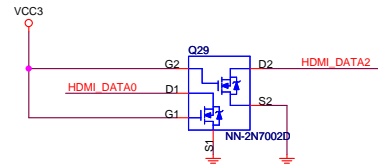
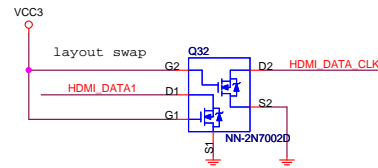
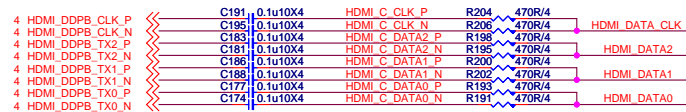
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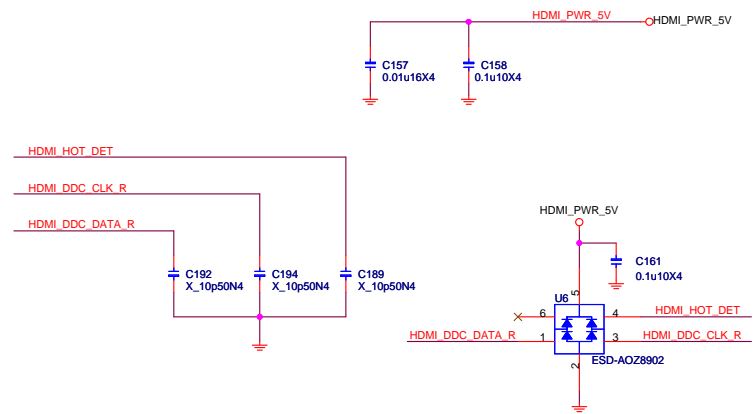
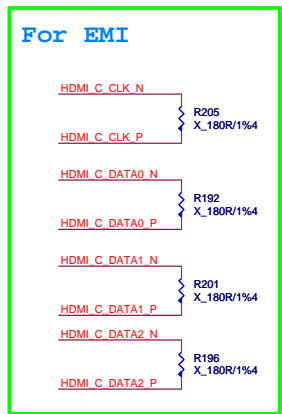
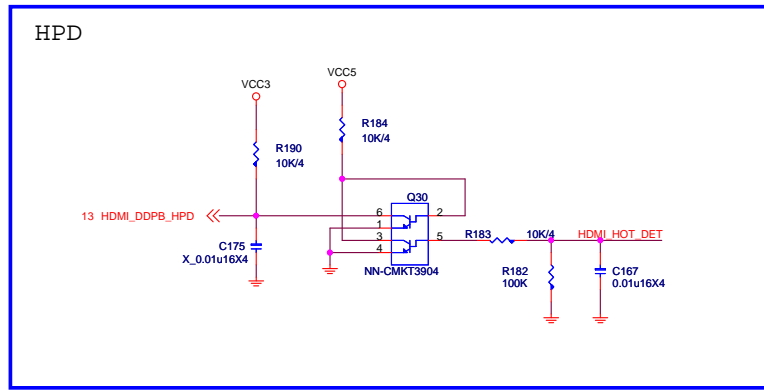
DVI max resolution is 1920x1200 @ 60 Hz



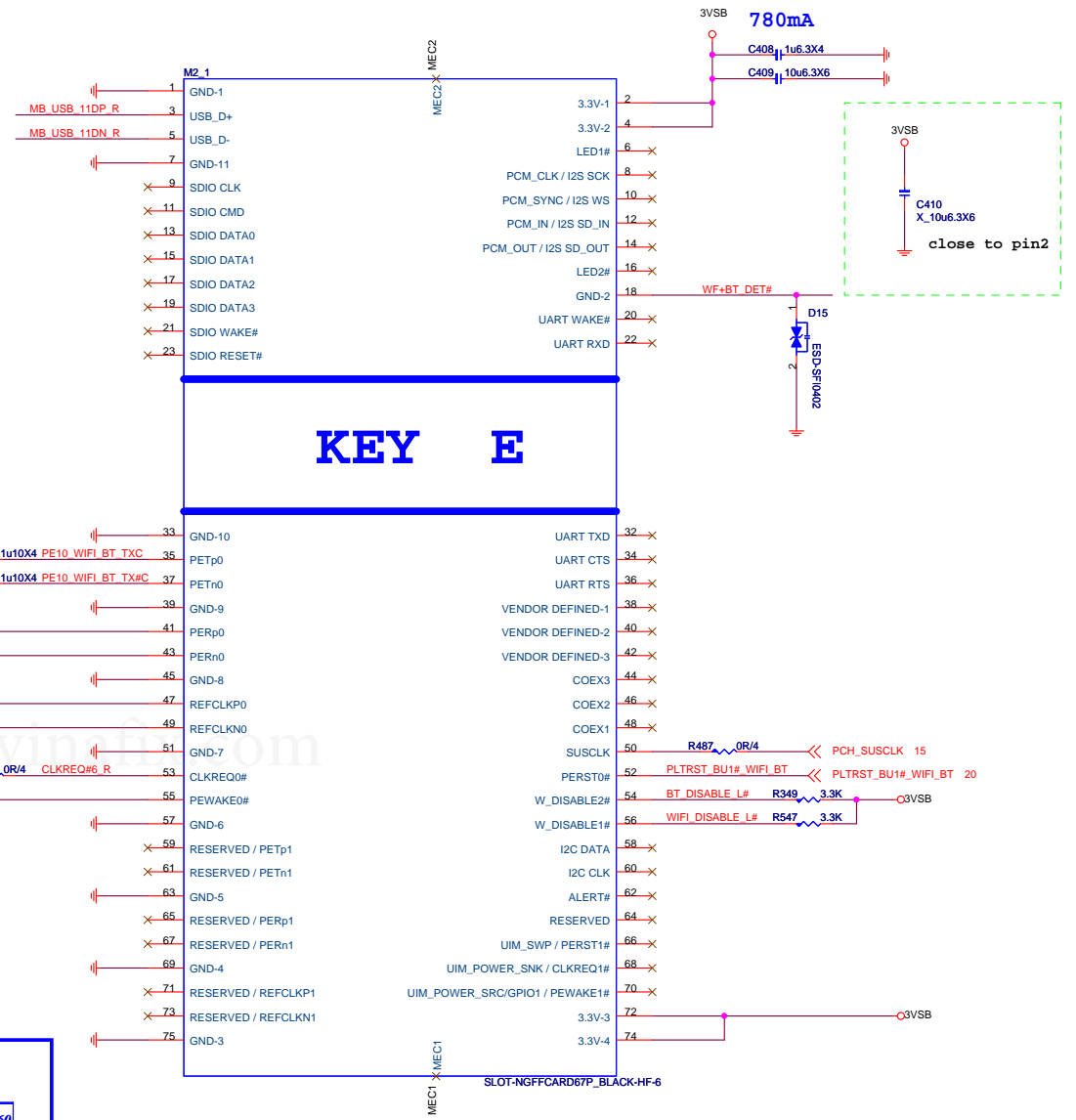
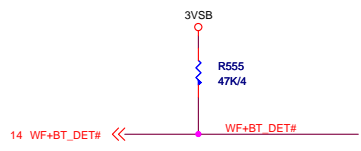
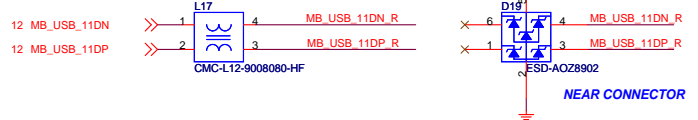
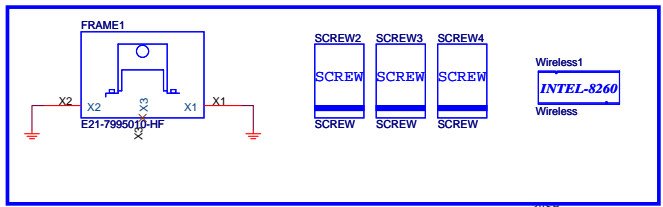
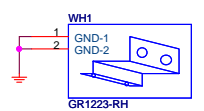
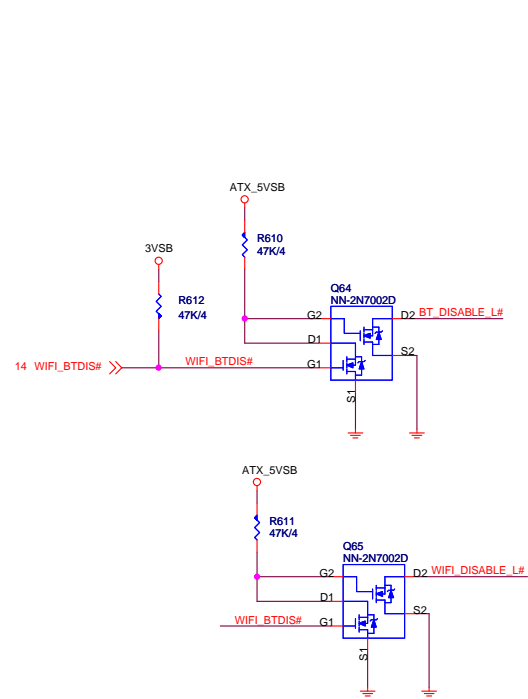
The HDMI\* interface supports HDMI with 3D, 4Kx2K@24Hz, Deep Color, and x.v.Color.



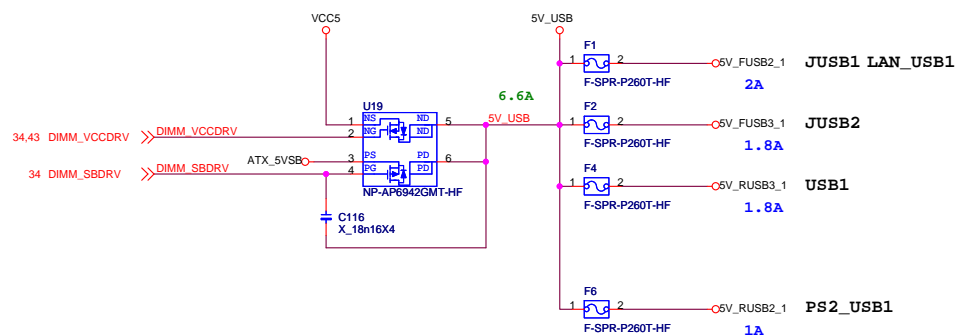
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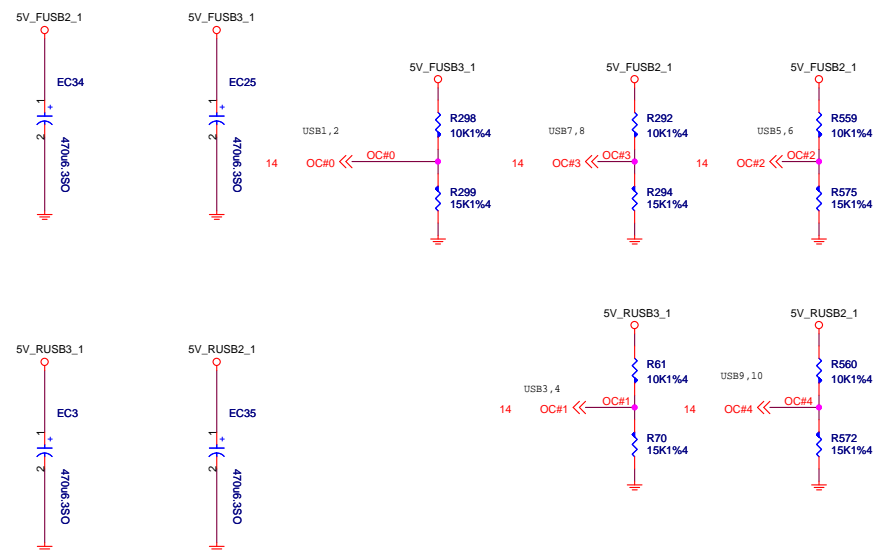




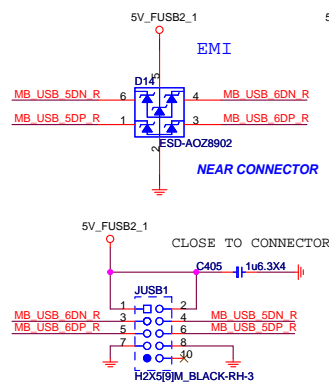
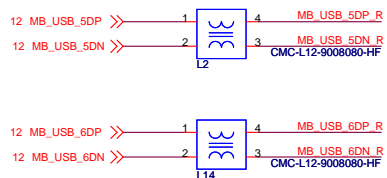
## USB POWER



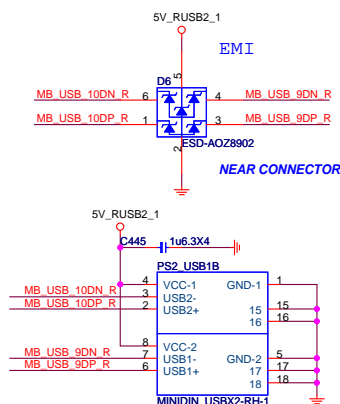
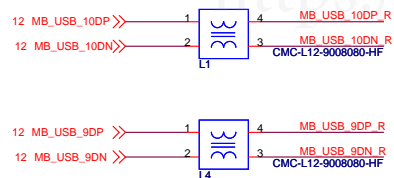
Vinafix.com



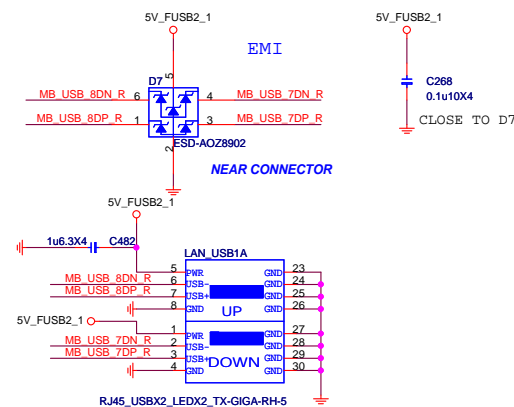
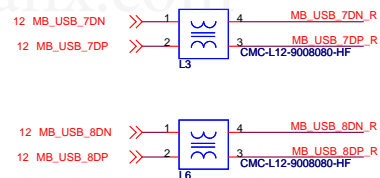
### FRONT USB PORT 5,6



## FRONT USB PORT 9,10



**FRONT USB PORT 7,8**

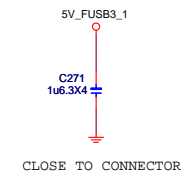
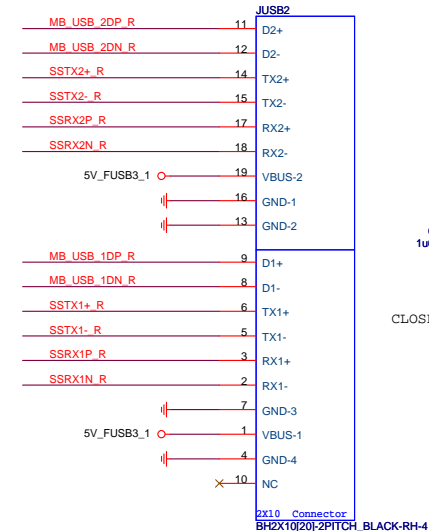
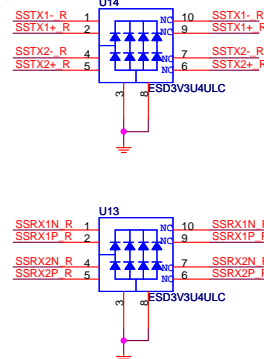
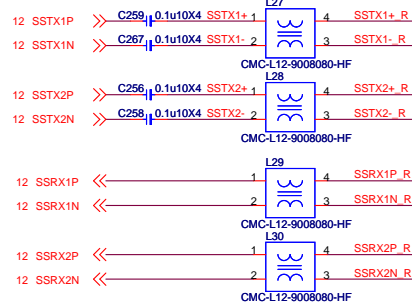
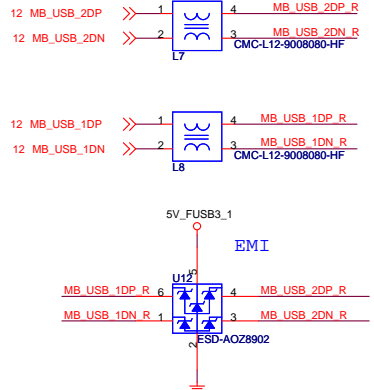
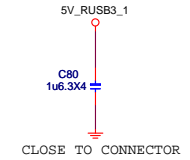
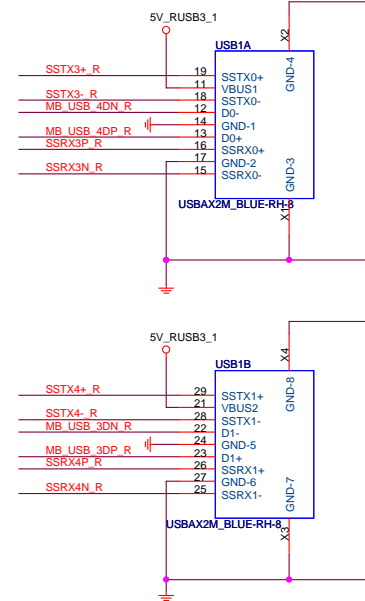
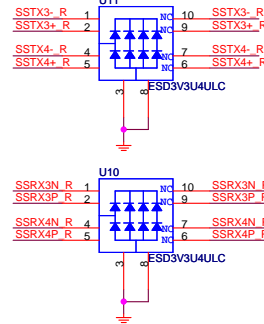
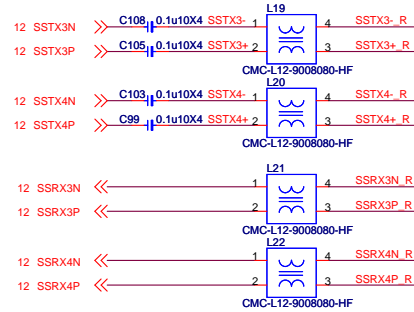
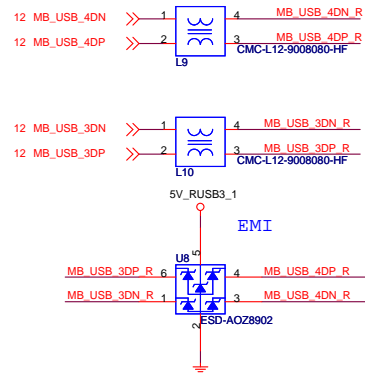


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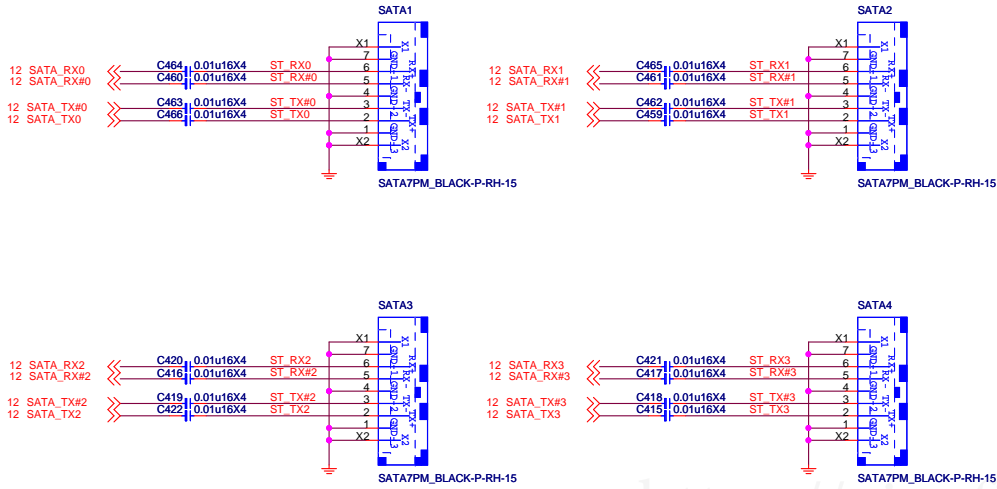
**MS-7995**

Size Custom	Document Description <b>USB2.0 Connector</b>	Rev 11
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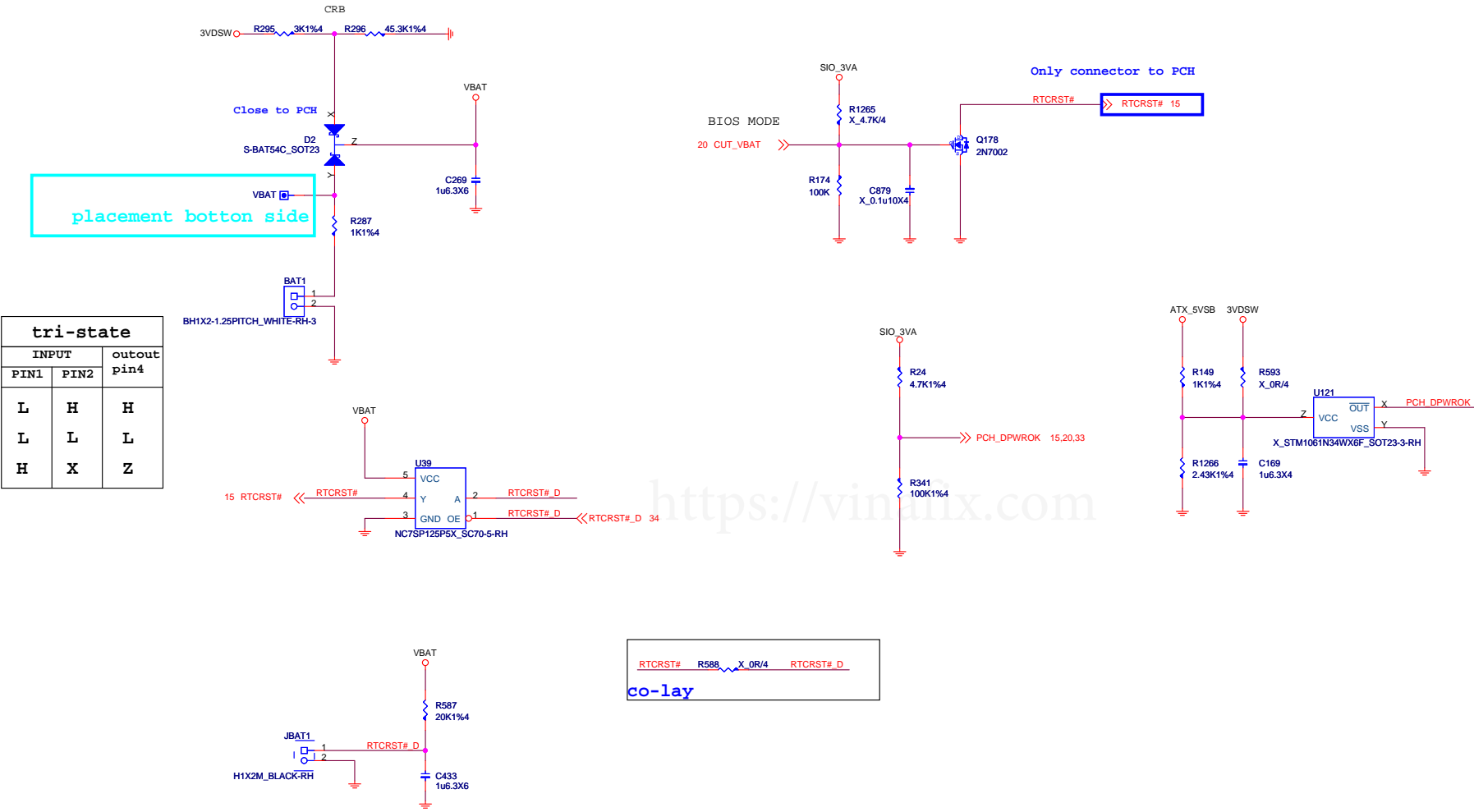




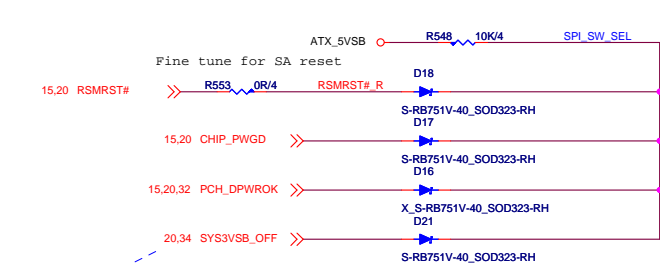
SATA GEN3



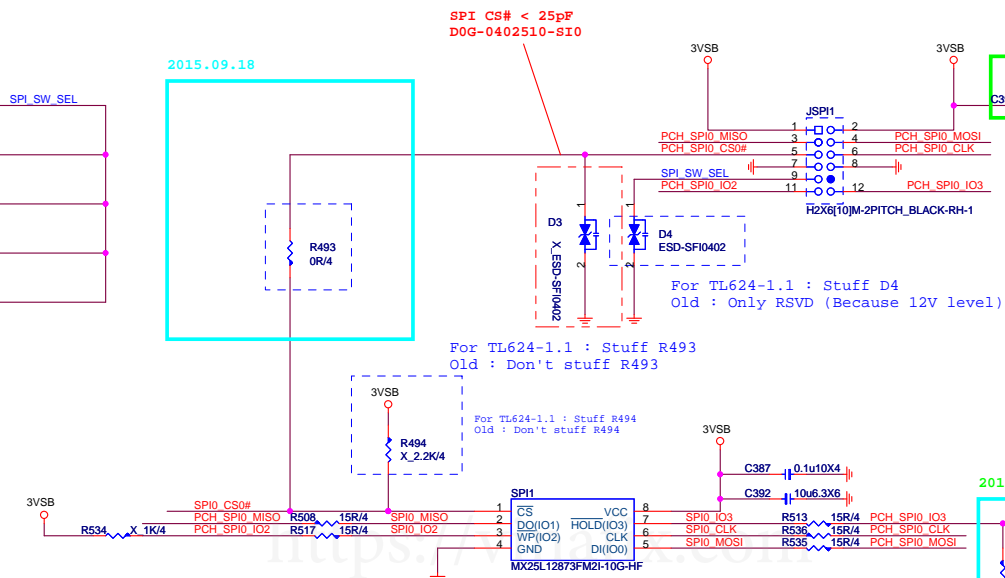
<https://vinafix.com>



15 PCH\_SPI0\_MOSI << PCH\_SPI0\_MOSI  
15 PCH\_SPI0\_MISO << PCH\_SPI0\_MISO  
15 PCH\_SPI0\_CLK << PCH\_SPI0\_CLK  
15 PCH\_SPI0\_CS0# << PCH\_SPI0\_CS0#  
15 PCH\_SPI0\_IO2 << PCH\_SPI0\_IO2  
15 PCH\_SPI0\_IO3 << PCH\_SPI0\_IO3



For TL624-1.1 (SKYLAKE)  
In skylake,PCH core is powered by VSB which need sink RSMRST#  
to low by SPI\_SW\_SEL.



- \* if you not support Standby power in S5 Status, component Q14.G Pull-high to +12V & Q14 MOS select 2N7002
- \* if you support Standby power in S5 Status(Ex; PCH is B75 Chipset), component Q14.G Pull-high to ATX\_5VSB, Q14 must select "Vth" under 1V (Component Suggestion as below)

D03-0341409-A68 / D03-0230019-A30

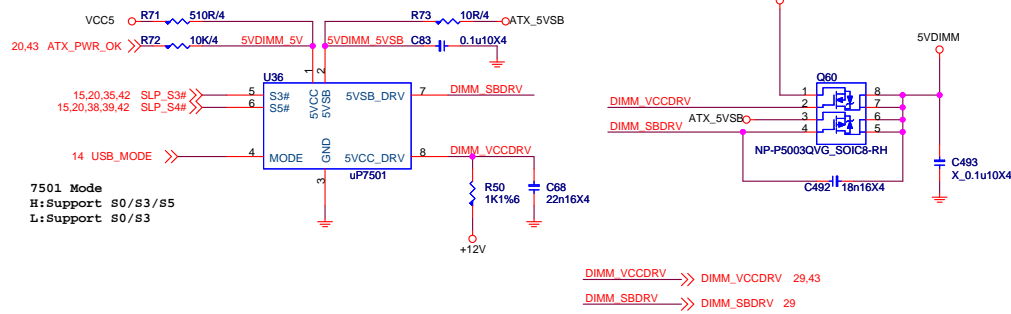


MICRO-STAR INT'L CO.,LTD

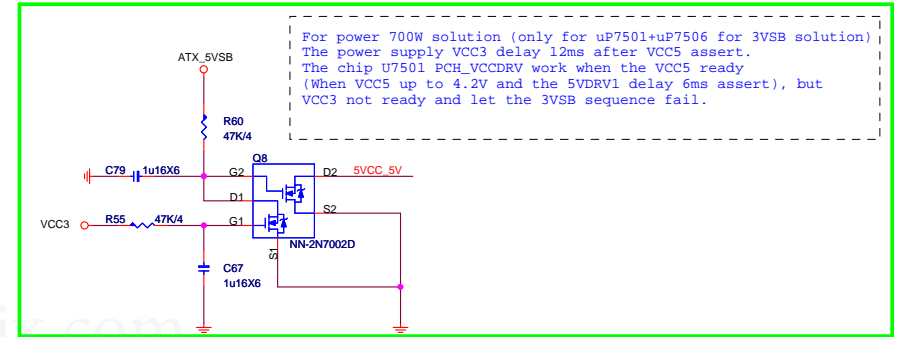
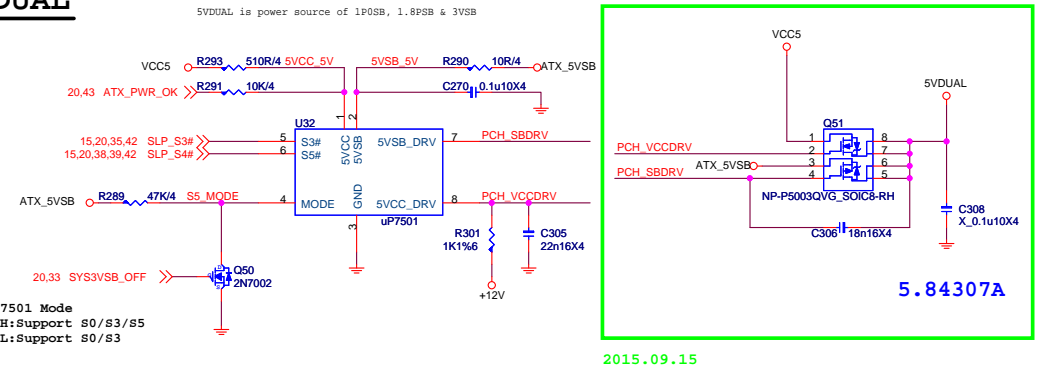
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Date: Monday, November 09, 2015	Sheet 33 of 50	

## 5VDIMM FOR DDR

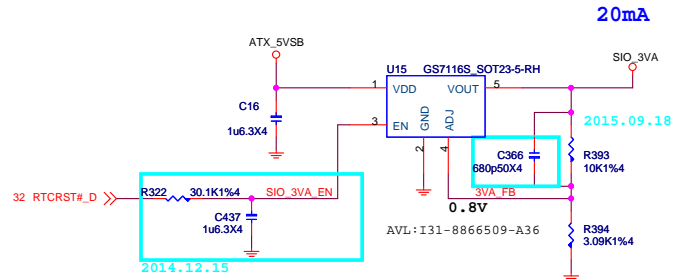


## 5VDUAL

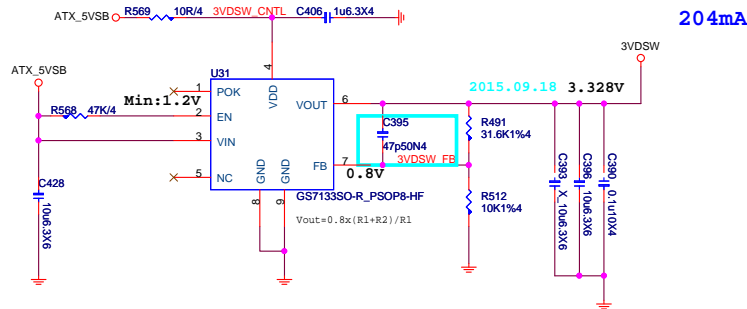


<https://vinafix.com>

## SIO\_3VA

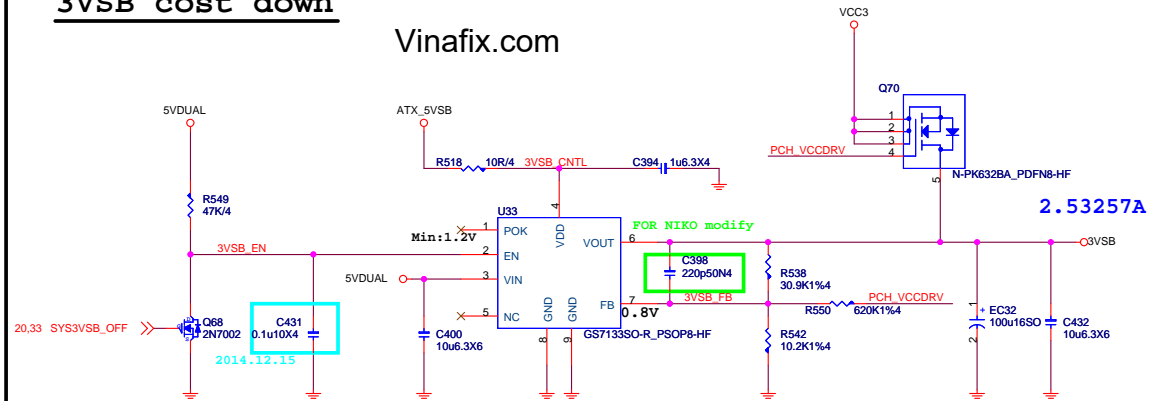


## 3VDSW



## 3VSB cost down

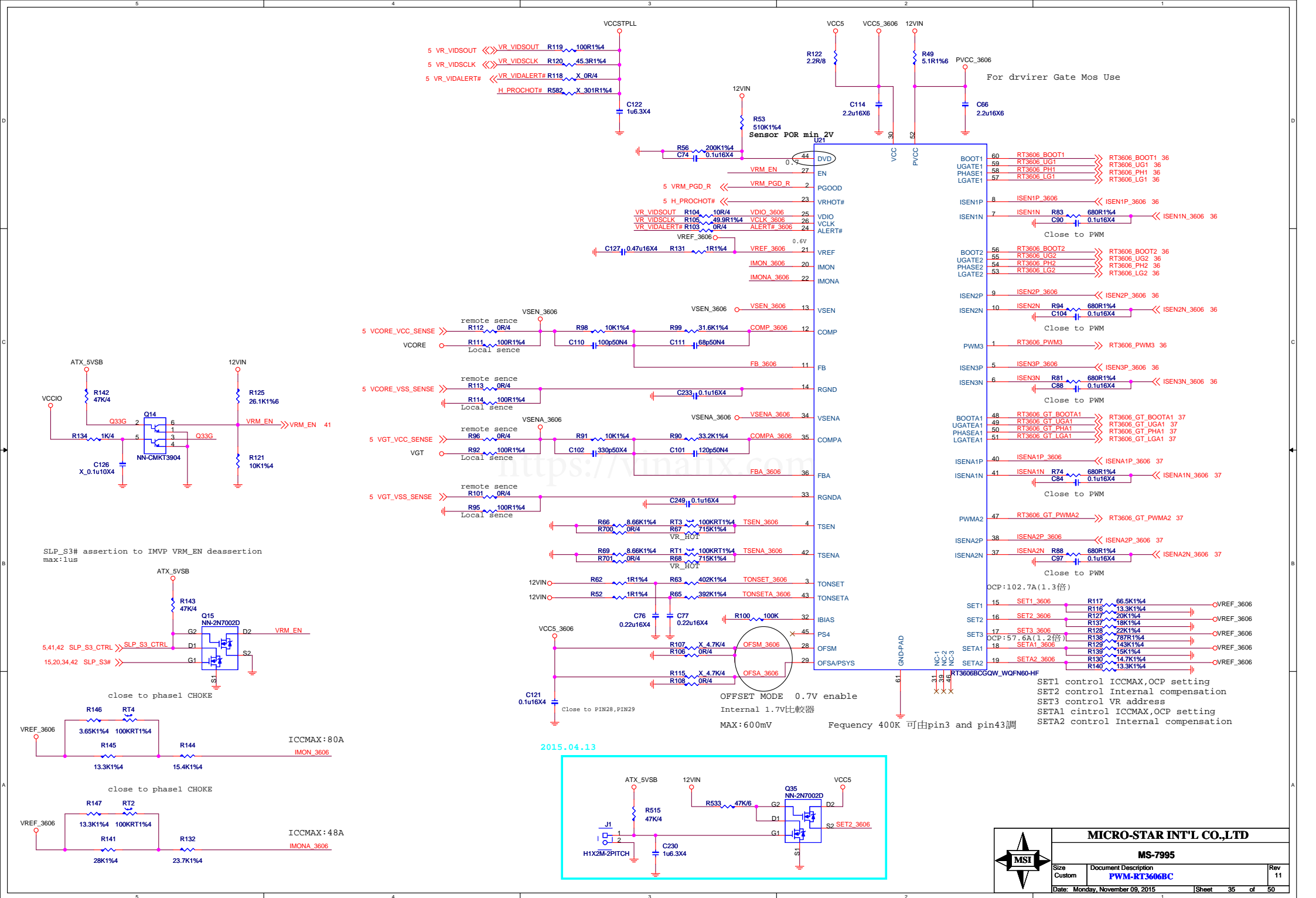
Vinafix.com



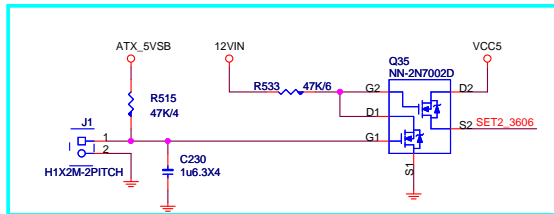
MICRO-STAR INT'L CO.,LTD

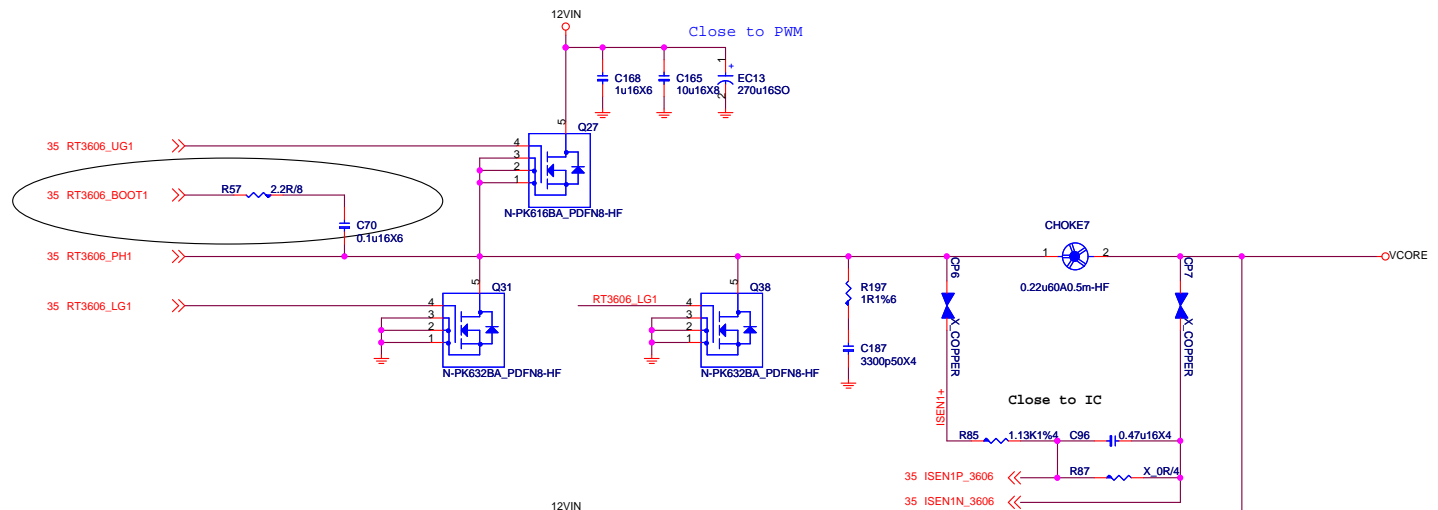
MS-7995

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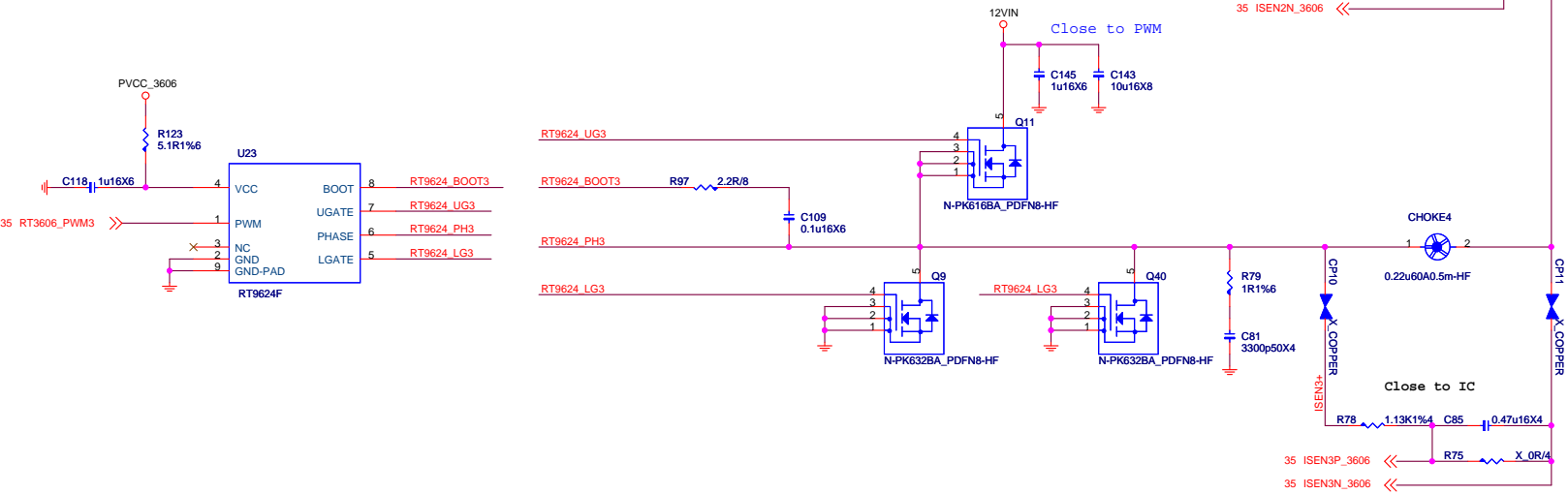
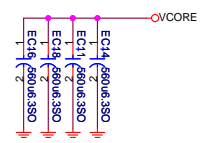
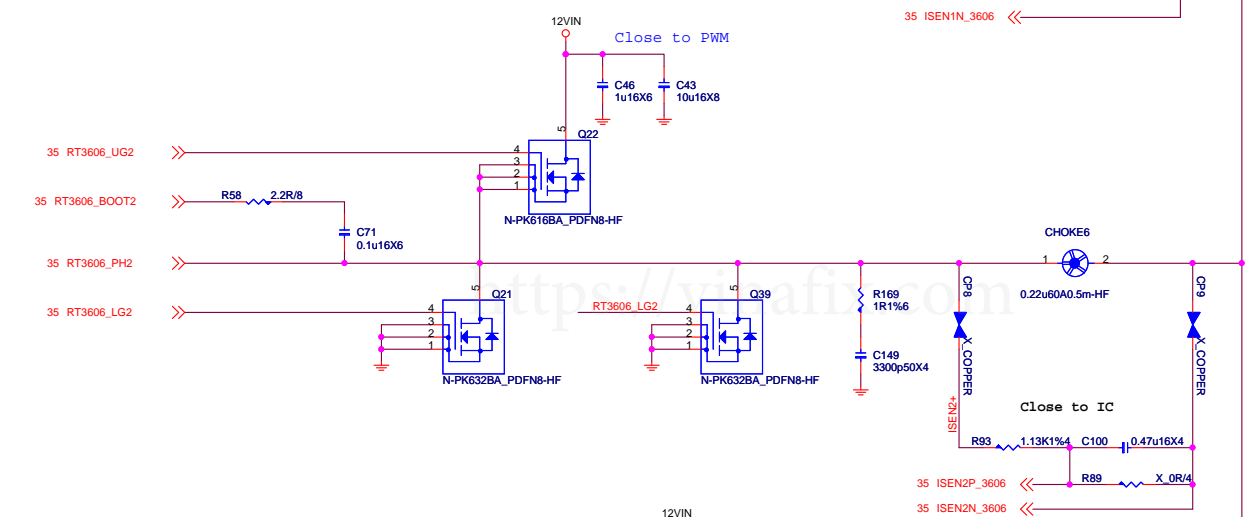


2015.04.13

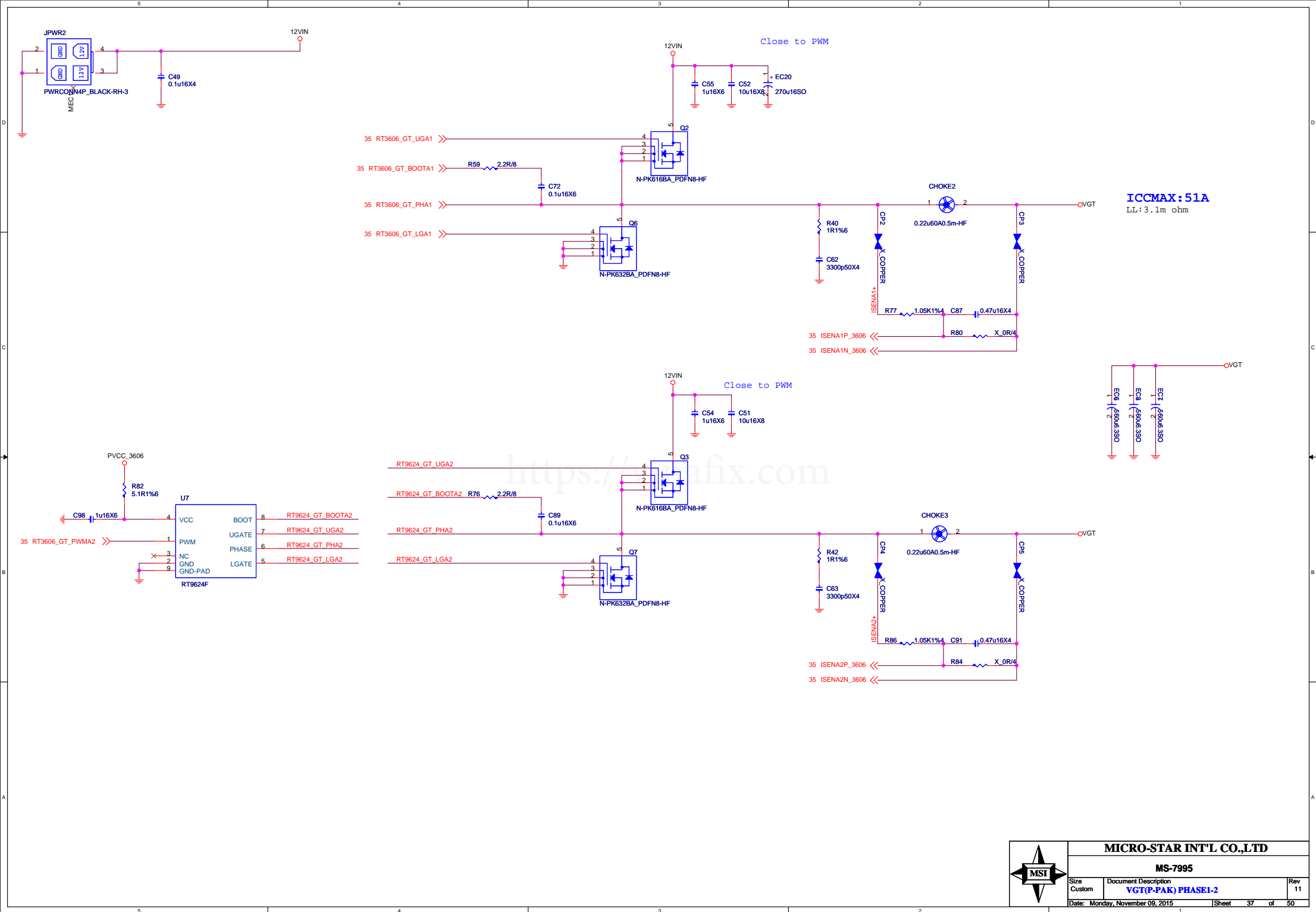




ICCMAX: 79A  
LL: 2.1m ohm





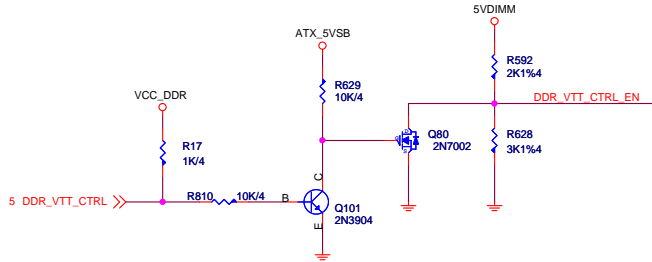
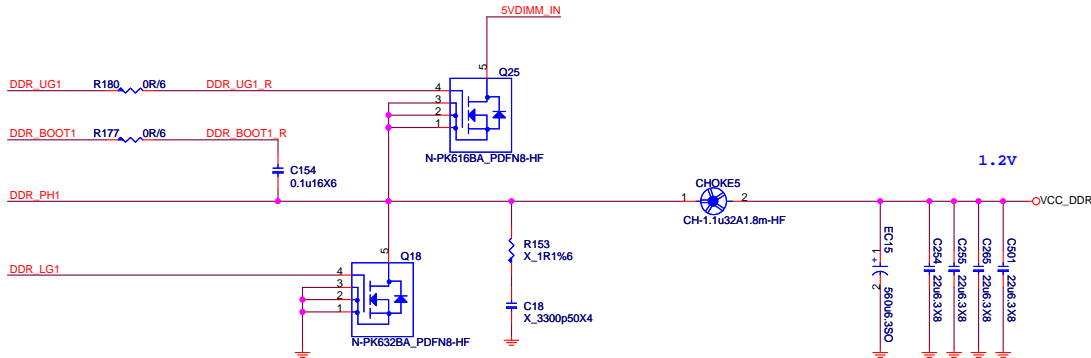
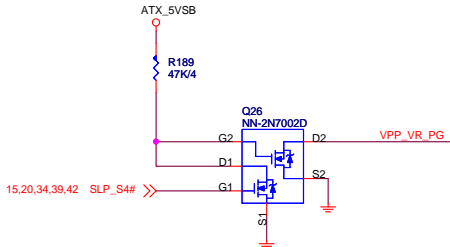
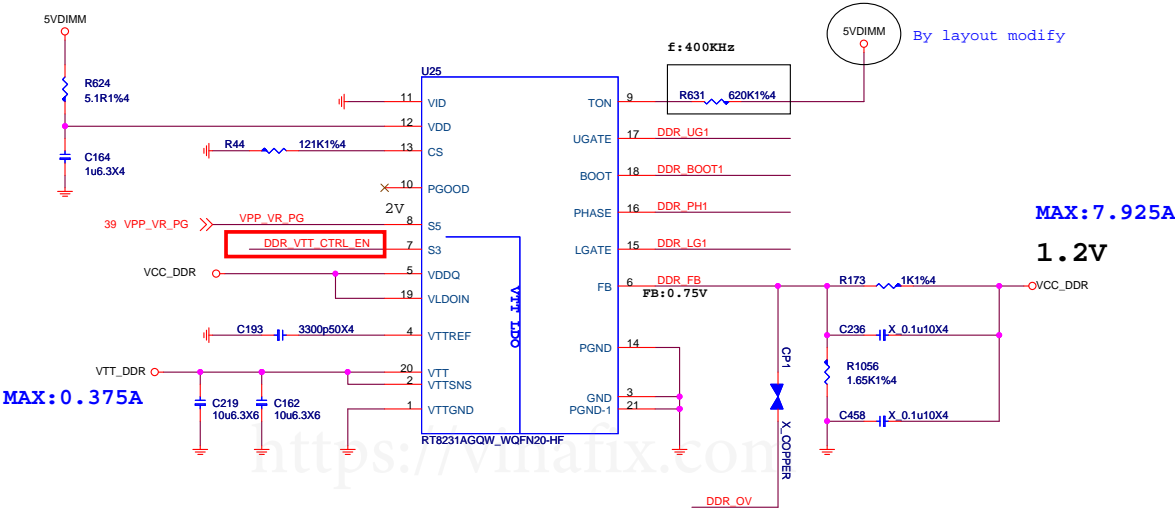
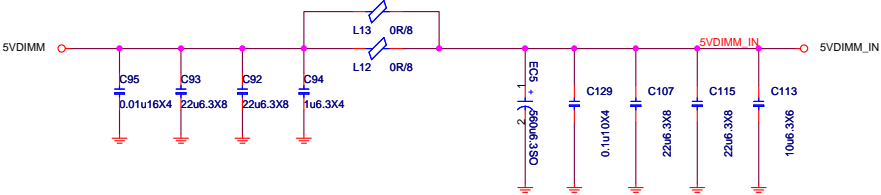


DDR4\_1.2V 2.8A+ 4.75A+0.375A=7.925

2.8A FOR CPU  
9.5A FOR 4DIMM DDR4  
4.75A FOR 2DIMM DDR4  
0.375A FOR VTT\_DDR

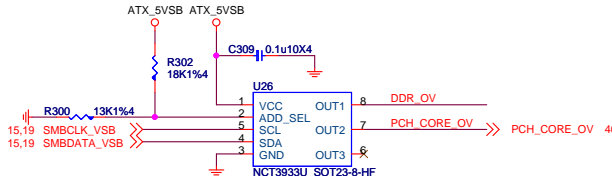
OCP = 7.925A\*1.5=11.8875A  
Rlimit(R44)  
Current Limit =121K\*5uA/10/5mohm  
--> 121Kohm(12.1A)

$$I_{rms} = I_{out} * \sqrt{((V_{out}/V_{in}) * (1 - (V_{out}/V_{in})))}$$
$$= 9.357 * 0.44$$
$$= 4.154A$$



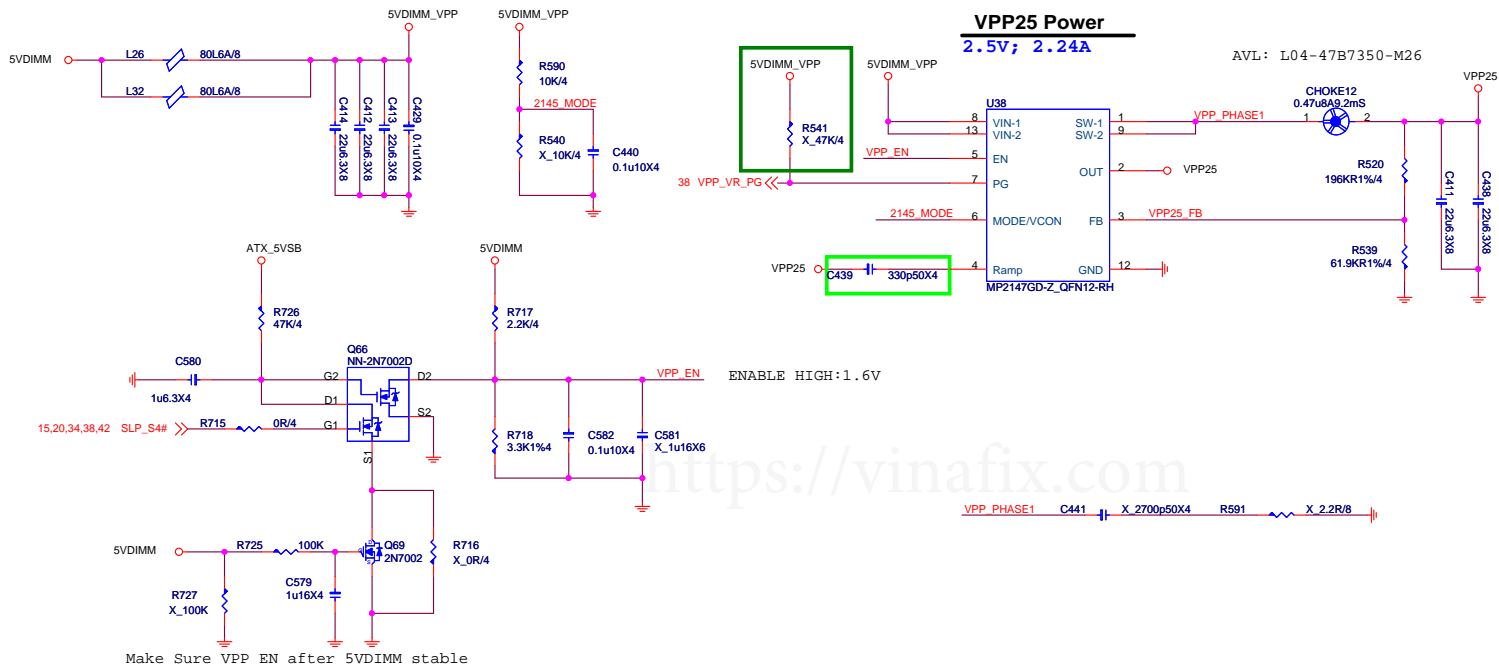
UPI VOLTAGE CONSOLE

0x26 : RH=18K, RL=13K



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**2DIMM :1.12A FOR DDR VPP2.5V**



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# PCH 1VSB

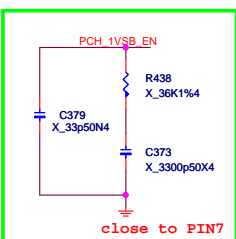
1.0V; 9.142A+5.5A=14.642A  
(6.54A + 0.132\*4+0.154\*6+0.054+0.132\*3)

OCP = 21.963A  
Rocset = 1.5 \* Imax \* Rdson(low) / Iocset  
= 1.5 \* 14.642 \* 5mohm / 10uA  
= 11.169K

Rocs:11.3K,OCP:  
D03-3669S00-F01 : 22.6A

Rdson(low) 4.5V  
D03-4C05N03-005 : 5 mohm  
D03-632BA0C-N03 : 4.6 mohm  
D03-3056M00-U47 : 6.2mohm  
D03-3669S00-F01 : 5.2m ohm

2014.08.22 close to U34

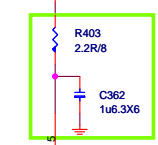
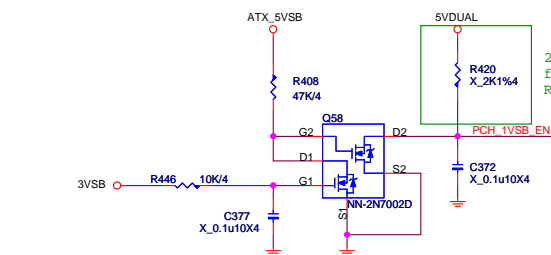


2015.01.22  
for up1540:stuff R438->36K,  
C379->NC,C373->3.3nF  
for RT8125:R438.C379.C373->NC

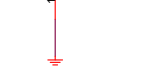
2014.12.25  
for up1540:C364&R407 ->NC

2014.12.25  
for up1540:C365 is OCP set min:5Kohm  
stuff 7.87K OCP SET:15.74A  
RT8125C stuff C1000P C11-1022032-W08

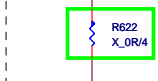
38 PCH\_CORE\_OV << PCH\_CORE\_OV  
to sink/source over voltage IC.  
pin10 sink/source current capability can't over 1mA  
So max voltage can't over 1.8V.  
from NCT3933



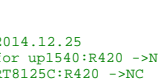
2015.01.22  
for up1540:R403->2.2R,C362->1uF  
for RT8125:R403->10R,C362->1uF



2014.12.25  
for up1540:R623 ->NC



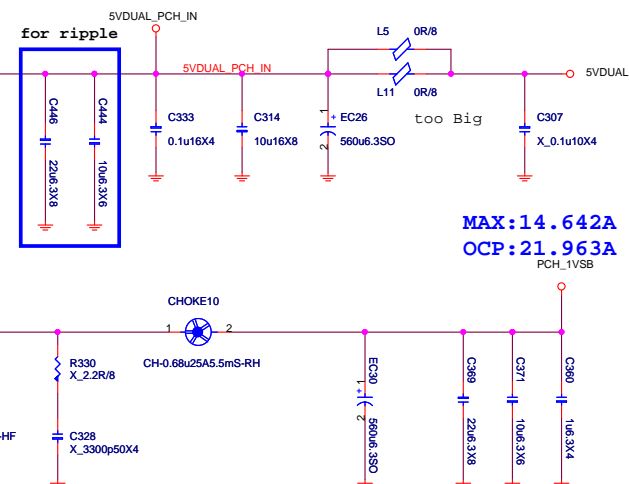
2014.12.25  
for up1540:stuff R622->0R



2014.12.25  
for up1540:R420 ->NC  
RT8125C:R420 ->NC

Vout = Vref \* (1 + R821/R822)  
= 0.8 \* (1 + 1K/3.92K)  
= 0.8 \* 1.2551  
= 1.004V

Irms = Iout \* SQRT((Vout/Vin) \* (1 - (Vout/Vin)))  
= 14.892 \* 0.4  
= 5.95686A



MAX:14.642A  
OCP:21.963A  
PCH\_1VSB

Lmin = ((Vin - Vout)/(Fsw \* k \* Iout\_max)) \* (Vout/Vin)  
= 0.59689uH (K = 30%)

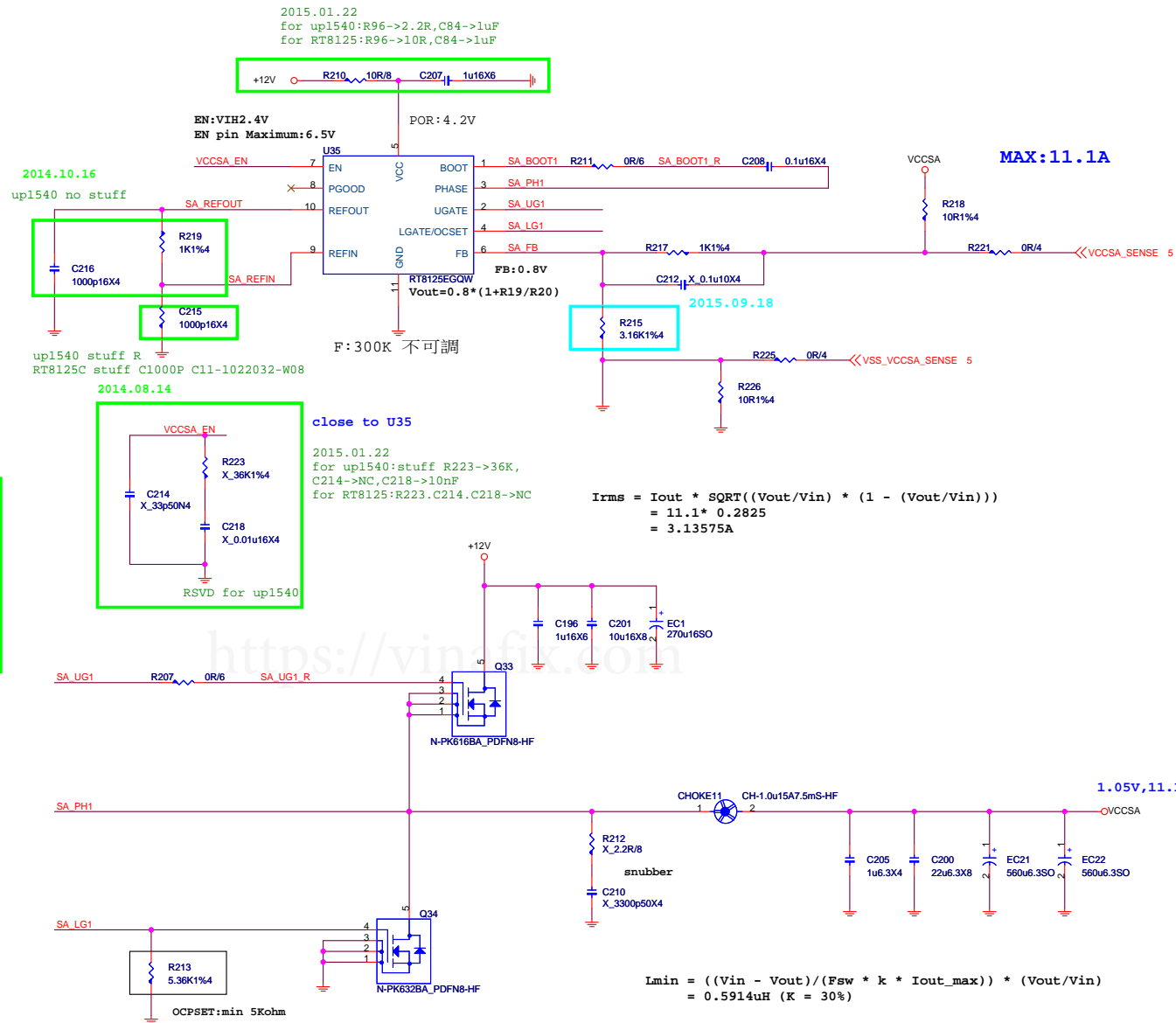
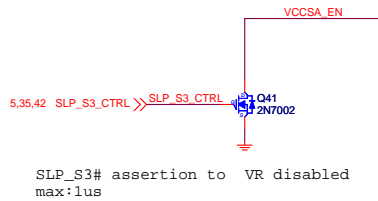
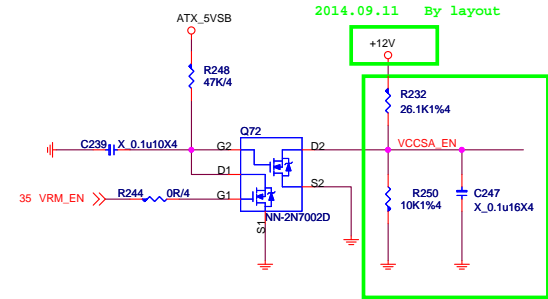
Vinafix.com

SA Power:1.05V,11.1A

$OCP = 11.1A * 1.4 = 15.54A$   
 $R_{cs}(R15) = OCP * R_{dson}(Low\ side) / 10uA$   
 $= 15.54 * (3.4)mohm / 10uA$   
 $= 5.2836Kohm$

Rcs:5.2836K,OCP:  
D03-4C05N03-005 : 15.76A  
D03-632BA0C-N03 : 16.24A  
use UBIQ MOS need Check

Rdson(10V)10V  
D03-4C05N03-005 : 3.4mohm  
D03-632BA0C-N03 : 3.3mohm  
D03-3056M00-U47 : 4.2mohm

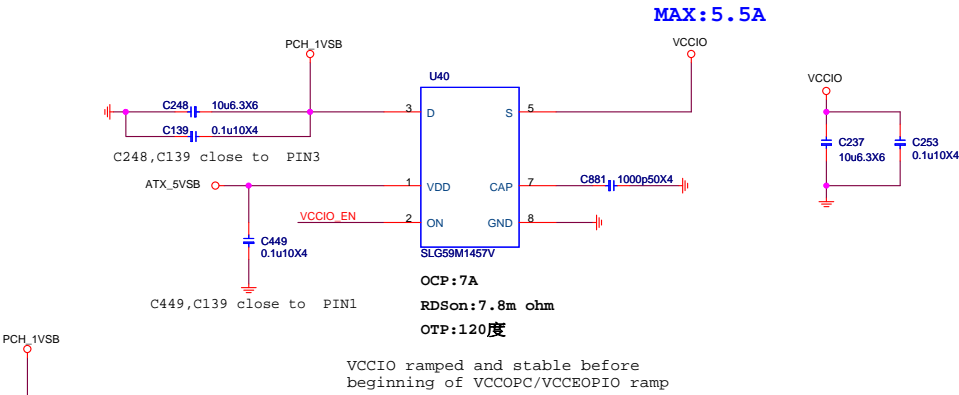


$$I_{rms} = I_{out} * \sqrt{((V_{out}/V_{in}) * (1 - (V_{out}/V_{in})))}$$
$$= 11.1 * 0.2825$$
$$= 3.13575A$$

$$L_{min} = ((V_{in} - V_{out}) / (F_{sw} * k * I_{out\_max})) * (V_{out}/V_{in})$$
$$= 0.5914uH (K = 30\%)$$

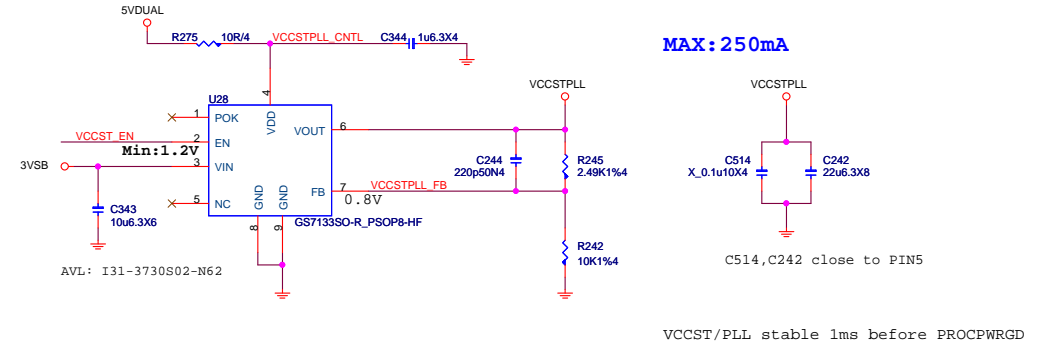
2015.10.29 change to SLG59M1457 load switch, Pwr source by PCH\_1VSB

VCCIO  
0.95V; 5.5A  
Source 1.00V

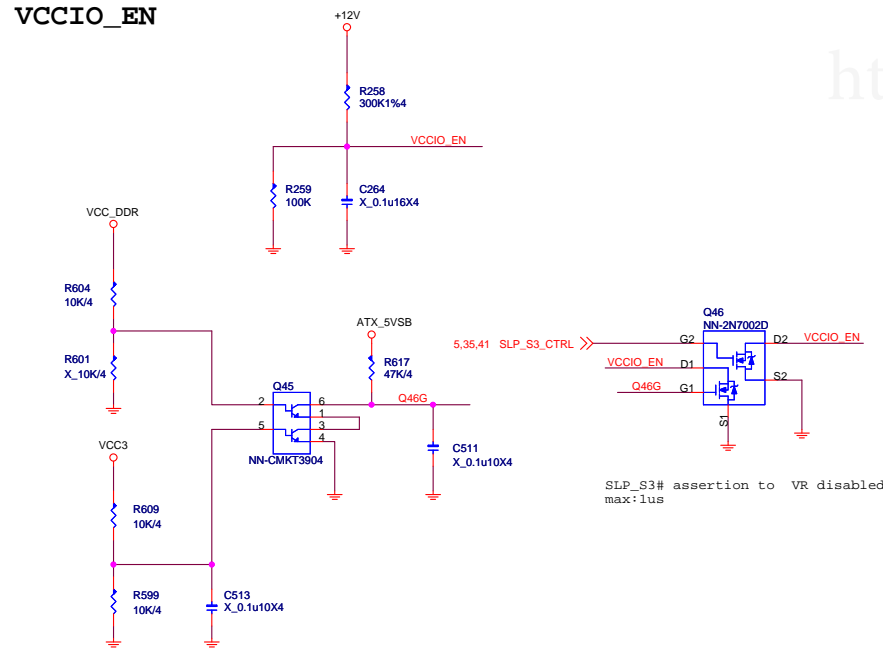


2015.10.29 change to GS7133

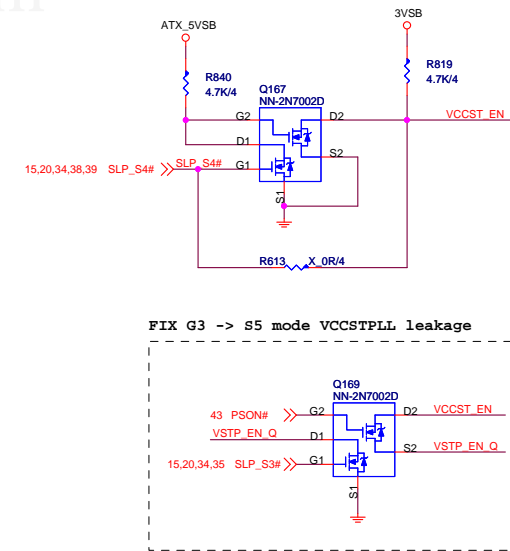
VCCSTPLL



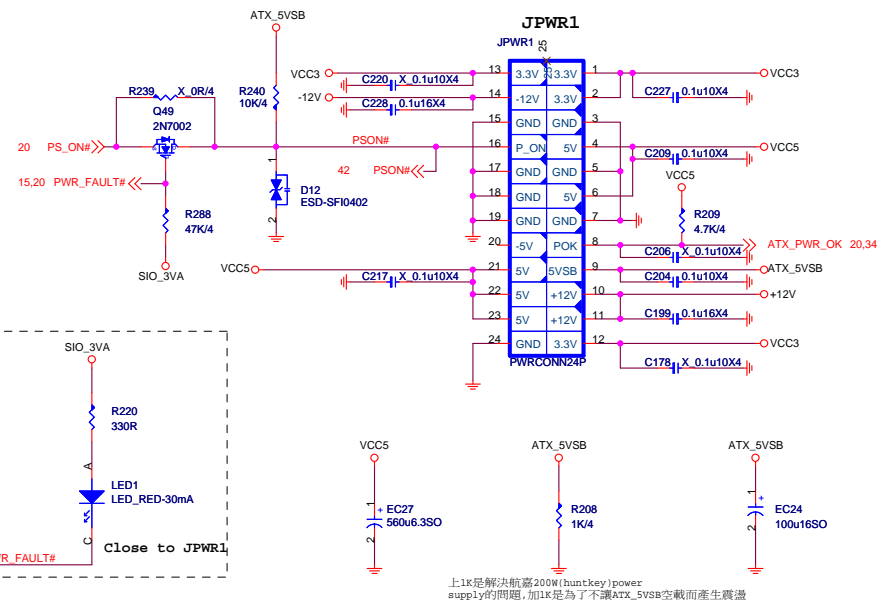
VCCIO\_EN



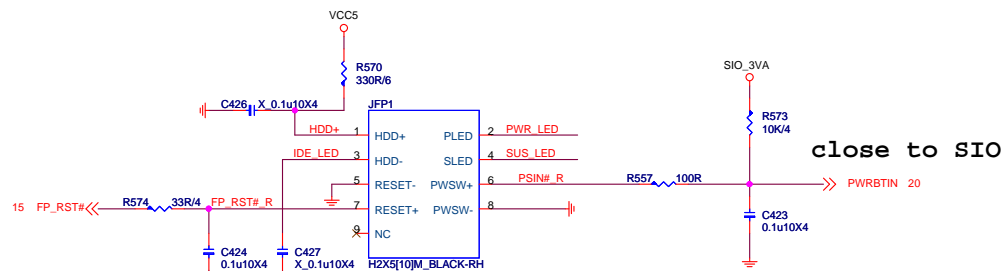
VCCSTPLL\_EN



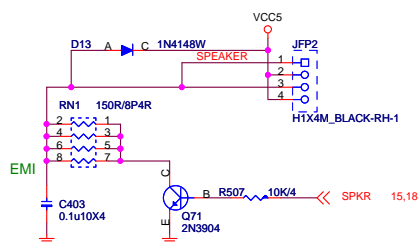
## ATX POWER CONNECTOR



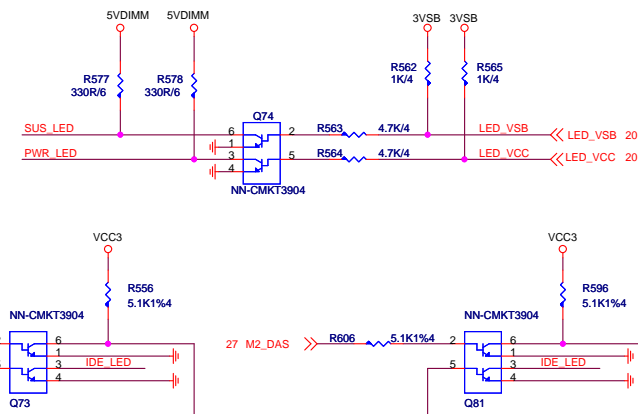
## FRONT PANNEL



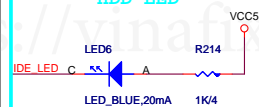
## Speaker Pin Header



## LED ( for NV5533)



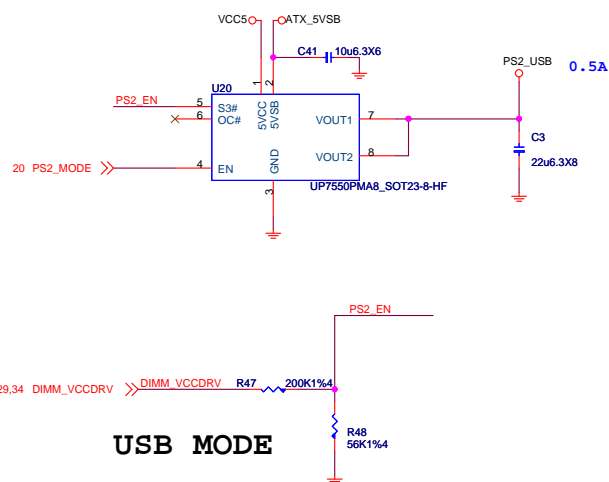
## HDD LED



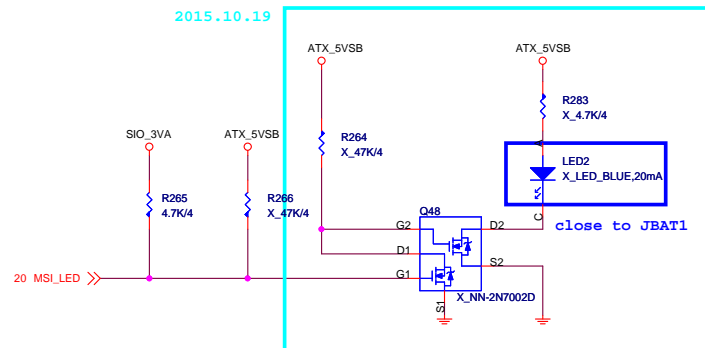
2015.09.21

<https://vinafab.com>

## PS2 POWER



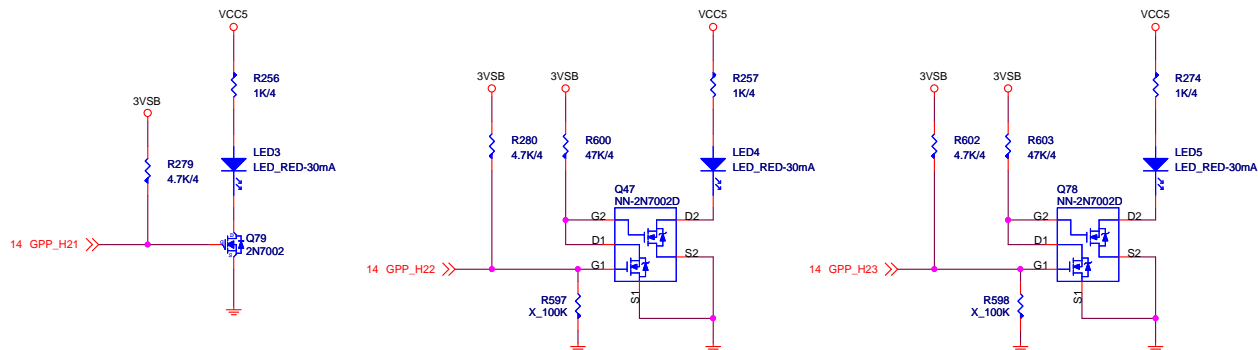
## MSI LED



## TPM

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DEBUG LED




GPIO LED	GPP_H21	GPP_H22	GPP_H23
亮	GPI PULL HIGH	GPO PO LOW	GPO PO LOW
滅	GPO LOW	GPO HIGH (default HIGH)	GPO HIGH (default HIGH)

- 關機斷電狀態下，3個LED先維持default全暗，開機通電後：
1. 首先進行CPU checkCPU LED 亮，check PASS後則CPU LED滅掉。
  2. 接著依序進行Memory /memory LED亮check PASS後則memory LED滅掉。
  3. VGA的check/VGA LED亮，check PASS後則VGA LED滅掉。
  4. 因此最後正常順利開機後，三個LED燈都是滅掉的。（系統重啟或其他原因造成系統重開機，則LED仍按上述行為動作）

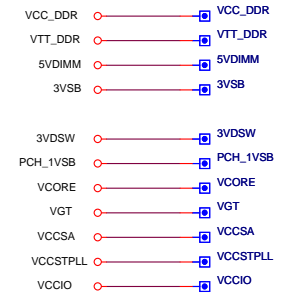
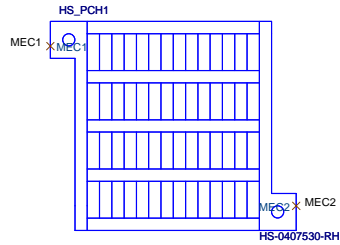


EMI CAP

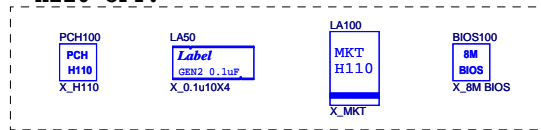
<https://vinafix.com>



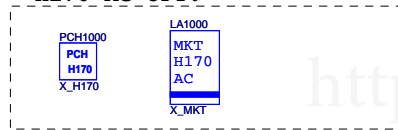
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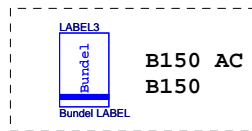
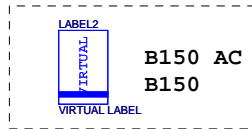
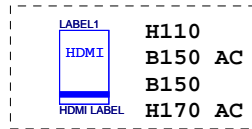
### H110 OPT.



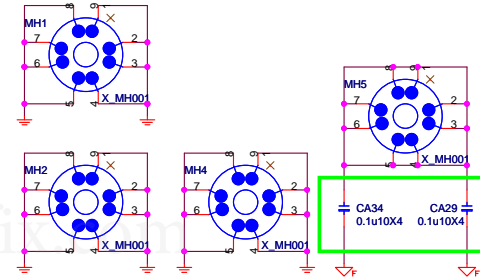
### H170 AC OPT.



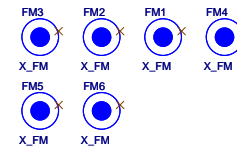
### B150 OPT.



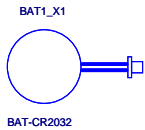
### Mounting Holes



### Optical Fiducial Marks-120



### Simulation



2015/10/52015/10/16PD0-0799510-E48,競華,23,寶安恩斯邁廠(MSIS)6,black  
2015/10/52015/10/16PD0-0799510-E48,競華,29,寶安恩斯邁廠(MSIS)6,black  
2015/10/52015/10/16PD0-0799510-G37,精成-深圳,23,寶安恩斯邁廠(MSIS)6,black  
2015/10/52015/10/16PD0-0799510-G37,精成-深圳,29,寶安恩斯邁廠(MSIS)6,black